

KAYPRO TECHNICAL MANUAL

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Part Number 1484-E

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1.0 INTRODUCTION

1.1 PURPOSE OF TECHNICAL MANUAL

This publication is intended to be a technical reference guide to be used by trained repair technicians. It will attempt to cover all dealer-serviceable sections of Kaypro computers. This manual replaces previous manuals on the subject.

The procedures and information contained in this manual assume technical expertise on the part of the reader. To avoid personal injury, do not perform any servicing unless you are a qualified service technician.

It is our desire to provide dealers with the information and support needed to expedite repairs and provide the users with the service they deserve. We encourage your comments and suggestions regarding this manual.

1.2 SCOPE OF TECHNICAL MANUAL

The information and procedures covered by this manual assume some technical knowledge on the part of the reader.

The policy of Kaypro Corporation is to repair computers to the modular level only. Even Kaypro's repair technicians do not repair switching power supplies, CRT assemblies, or disk drives. Repairs to modular components not manufactured by Kaypro Corporation (power supplies, CRT assemblies, disk drives) are NOT covered by this manual.

However, we do not discourage dealers and technicians who have the knowledge and the tools to repair to the component level from doing so, on out-of-warranty Kaypro computers.

The adjustment and removal/replacement information in this manual is organized by module type, with the exception of removal/replacement information for the KAYPRO ROBIE, which is placed in a separate section due to the differences in chassis design and hardware module placement in that machine.

1.3 SERVICE WARNING

Proper precautions must be observed to assure that components are not damaged when a unit is serviced or adapters are added to or removed from the I/O channels.

The preferred method is to have a grounding strap attached to the wrist with the drain lead connected to a common earth ground, and to have the unit positioned on a conductive grounded mat. With the above precautions observed, and the power switched off, one may insert or remove components or adapters.

If one has built up a charge, this may be felt when first contacting the aforementioned surfaces.

Failure to observe these precautions may damage the unit in such a manner as to render it unserviceable or unreliable.

2.0 FCC INFORMATION

As Kaypro keeps in step with computer technology, the models have changes which affect FCC ratings. The proper rating is affixed to the back of each computer. Contact the Kaypro Engineering Department if you need further information.

3.0 MODEL SPECIFICATIONS

3.1 KAYPRO 2 SPECIFICATIONS

CPU	Z-80, 2.5 MHz
RAM	64K bytes
MAINBOARD	81-110-n or 81-240-n series.
DISK STORAGE	Two 5-1/4 inch, double-density, single-sided, floppy disk drives, providing 191K bytes of storage per diskette.
KEYBOARD	Detachable, 72 key typewriter style keyboard with 18 programmable keys.
VIDEO SCREEN	Non-glare, 9-inch, green phosphor screen with a 25 row x 80 column display.
I/O CONNECTIONS	One "Centronics"-type parallel port, one RS-232C serial port.

3.2 KAYPRO 2/84 and 2X SPECIFICATIONS

CPU	Z80-A, 4.0 MHz
RAM	64K bytes
MAINBOARD	81-294-n series.
DISK STORAGE (Kaypro 2/84)	Two 5-1/4 inch double-density, single-sided, floppy disk drives, providing 191K bytes of storage per diskette.
DISK STORAGE (Kaypro 2X)	Two 5-1/4 inch double-density, double-sided, floppy disk drives, providing 390K bytes of storage per diskette.
KEYBOARD	Detachable, 72 key typewriter style keyboard with 18 programmable keys.
VIDEO SCREEN	Non-glare, 9-inch, green phosphor screen with a 25 row x 80 column display.
I/O CONNECTIONS	One "Centronics"-type parallel port, two RS-232C serial ports.

3.3 KAYPRO 4 SPECIFICATIONS

CPU	Z-80, 2.5 MHz
RAM	64K bytes
MAINBOARD	81-240-n series.
DISK STORAGE	Two 5-1/4 inch double-density, double-sided, floppy disk drives, providing 390K bytes of storage per diskette.
KEYBOARD	Detachable, 72 key typewriter style keyboard with 18 programmable keys.
VIDEO SCREEN	Non-glare, 9-inch, green phosphor screen with a 25 row x 80 column display.
I/O CONNECTIONS	One "Centronics"-type parallel port, one RS-232C serial port.

3.4 KAYPRO 4/84 SPECIFICATIONS

CPU	Z-80A, 4.0 MHz
RAM	64K bytes
MAINBOARD	81-184-n series.
DISK STORAGE	Two double-density, double-sided, floppy disk drives, providing 390K bytes of storage per diskette.
KEYBOARD	Detachable, 72 key typewriter style keyboard with 18 programmable keys.
VIDEO SCREEN	Non-glare, 9-inch green phosphor screen with a 25 row x 80 column display.
I/O CONNECTIONS	One "Centronics"-type parallel port, two RS-232C serial ports, one RJ11C modular telephone jack.
MODEM	Built-in, 300-baud modem, with Bell System 103 compatibility. Uses Texas Instruments TMS99531/TMS99532.
REAL-TIME CLOCK	Built-in real-time clock. Uses National MM58167.

3.5 KAYPRO 4X SPECIFICATIONS

CPU	Z-80A, 4.0 MHz
RAM	64K bytes
MAINBOARD	81-296-n series.
DISK STORAGE	Two 5-1/4 inch, high-density, double-sided, floppy disk drives providing 2.6M bytes of storage per diskette.
KEYBOARD	Detachable, 72 key typewriter style keyboard with 18 programmable keys.
VIDEO SCREEN	Non-glare, 9-inch, green phosphor screen with a 25 row x 80 column display.
I/O CONNECTIONS	One "Centronics"-type parallel port, two RS-232C serial ports, one RJ11C modular telephone jack.
MODEM	Built-in, 300-baud modem, with Bell System 103 compatibility. Uses Texas Instruments TMS99531/TMS99532.
REAL-TIME CLOCK	Built-in real-time clock. Uses National MM58167.

3.6 KAYPRO 10 SPECIFICATIONS

CPU	Z80A, 4.0 MHz
RAM	64K bytes
MAINBOARD	81-180-n series.
DISK STORAGE	One 5-1/4 inch double-density, double-sided, floppy disk drive providing 390K bytes of storage per diskette. One hard disk drive providing 10M bytes of storage.
KEYBOARD	Detachable, 72 key typewriter style keyboard with 18 programmable keys.
VIDEO SCREEN	Non-glare, 9-inch, green phosphor screen with a 25 row x 80 column display.
I/O CONNECTIONS	One "Centronics"-type parallel port, two RS-232C serial ports.

3.61 KAYPRO 10 with CLOCK and MODEM SPECIFICATIONS

CPU	Z80A, 4.0 MHz
RAM	64K bytes
MAINBOARD	81-583-n series.
DISK STORAGE	One 5-1/4 inch double-density, double-sided, floppy disk drive providing 390K bytes of storage per diskette. One hard disk drive providing 10M bytes of storage.
KEYBOARD	Detachable, 72 key typewriter style keyboard with 18 programmable keys.
VIDEO SCREEN	Non-glare, 9-inch, green phosphor screen with a 25 row x 80 column display.
I/O CONNECTIONS	One "Centronics"-type parallel port, two RS-232C serial ports.
MODEM	Built-in, 300 baud modem, with Bell System 103 compatibility. Uses Texas Instruments TMS99531/TMS99532.
REAL-TIME CLOCK	Built-in real-time clock. Uses National MM58167.

3.7 KAYPRO ROBE SPECIFICATIONS

CPU	Z80A, 4.0 MHz
RAM	64K bytes
MAINBOARD	81-296-n series.
DISK STORAGE	Two 5-1/4 inch, high-density, double-sided, floppy disk drives providing 2.6M bytes of storage per diskette.
KEYBOARD	Detachable, 72 key typewriter style keyboard with 18 programmable keys.
VIDEO SCREEN	Non-glare, 9-inch, green phosphor screen with a 25 row x 80 column display.
I/O CONNECTIONS	One "Centronics" type parallel port, two RS232C serial ports, one RJ11C modular telephone jack.
MODEM	Built-in, 300-baud modem, with Bell System 103 compatibility. Uses Texas Instruments TMS99531/TMS99532.
REAL-TIME CLOCK	Built-in real-time clock. Uses National MM58167.

3.71 KAYPRO 12X SPECIFICATIONS

CPU	Z80A, 4.0 MHz
RAM	64K bytes
MAINBOARD	81-471-n series.
DISK STORAGE	One 5-1/4 inch high-density, double-sided, floppy disk drive providing 2.6M bytes of storage per diskette. One hard disk drive providing 10M bytes of storage.
KEYBOARD	Detachable, 72 key typewriter style keyboard with 18 programmable keys.
VIDEO SCREEN	Non-glare, 9-inch, green phosphor screen with a 25 row x 80 column display.
I/O CONNECTIONS	One "Centronics"-type parallel port, two RS-232C serial ports.
MODEM	Built-in, 300 baud modem, with Bell System 103 compatibility. Uses Texas Instruments TMS99531/TMS99532.
REAL-TIME CLOCK	Built-in real-time clock. Uses National MM58167.

3.8 KAYPRO NEW 2 SPECIFICATIONS

CPU	Z-80A, 4.0 MHz
RAM	64K bytes
MAINBOARD	81-294-n series.
DISK STORAGE	One 5-1/4 inch double-density, double-sided, floppy disk drive, providing 390K bytes of storage per diskette.
KEYBOARD	Detachable, 72 key typewriter style keyboard with 18 programmable keys.
VIDEO SCREEN	Non-glare, 9-inch, green phosphor screen with a 25 row x 80 column display.
I/O CONNECTIONS	One "Centronics"-type parallel port, two RS-232C serial ports.

3.9 KAYPRO 1 SPECIFICATIONS

CPU	Z80A, 4.0 MHz
RAM	64K bytes
MAINBOARD	81-809-n series.
DISK STORAGE	Two 5-1/4 inch double-density, double-sided, floppy disk drive providing 390K bytes of storage per diskette.
KEYBOARD	Detachable, 72 key typewriter style keyboard with 18 programmable keys.
VIDEO SCREEN	Non-glare, 9-inch, green phosphor screen with a 25 row x 80 column display.
I/O CONNECTIONS	One "Centronics"-type parallel port, two RS-232C serial ports.

4.0 KAYPRO ROM REVISION—CP/M VERSION COMPATIBILITY

MODEL NAME	CP/M VERSION	MAINBOARD	ROM REVISION
2/83	2.2f	81-110	81-149-C
4/83	2.2f	81-240	81-232-A
4/83 + 88	2.2f	81-240	81-232-A
2/84	2.2g	81-294	81-292-A
4/84	2.2g	81-184	81-292-A
4/84 + 88	2.2g	81-184	81-292-A
2X	2.2g 2.2h	81-294	81-292-A
4X	2.2g 2.2h	81-297	81-326-E
ROBIE	2.2g 2.2h	81-296	81-478-A
10	2.2g 2.2h	81-180	81-302-C
2X W/MODEM and CLOCK	2.2u1	81-580	81-478-A
10 W/MODEM and CLOCK	2.2u1	81-582	81-478-A
NEW 2	2.2u1	81-294	81-478-A
1	2.2u1	81-294	81-478-A

---> **TAB FOR THIS SECTION: HARDWARE**
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5.0 CHASSIS

5.1 CHASSIS HOOD REMOVAL (EXCEPT KAYPRO ROBIE)

1. Turn off the machine.
2. Disconnect AC power by unplugging the power cord from wall outlet.
3. Remove the ten screws from the chassis hood; there are two on top and four on each side.
4. Remove the hood from the chassis.

CHASSIS HOOD REPLACEMENT

1. Lower the hood onto the unit.
2. Align the two holes on top of the hood with those on top of the chassis.
3. Insert the two flat-head screws into the holes on the top of the chassis and start them, but do not tighten them yet.
4. Insert the eight round-head screws, four on each side, and start them.
5. Tighten each screw securely.

5.2 TOUCH-UP INFORMATION

Kaypro Corporation has small amounts of touch-up paint for Kaypro hoods and chassis available to the dealers. Contact the Hardware Technical Support personnel to obtain this paint.

Occasionally a customer's computer will have scratches on the hood or chassis. A small amount of rubbing compound, when carefully applied, will often smooth out very small scratches on a hood or chassis. Should painting be necessary, there are two sizes of sable paintbrushes to have on hand: size 00 and size 000. These brushes are available at any art supply store.

6.0 MAINBOARDS

The following sections contain schematics, chip layout diagrams, and IC lists (by U-number) for Kaypro mainboards. This is not intended to be a theory of operation, but rather an aid in locating possible problems on a mainboard.

Consult an appropriate databook (TTL, Zilog, etc.) if you need to find out the internal workings of a particular IC.

MAINBOARD TROUBLESHOOTING TIPS:

A blank EPROM can be used to help troubleshoot Kaypro mainboards of series 81-110, 81-240, and 81-180. The appropriate model of EPROM (2732, 2716, etc) allows a quick check of the board. It will usually force the data and address lines to toggle at approximately the same amplitude, allowing the technician to use a scope to spot affected lines.

If the video display does not show a screen filled with alternating "9"s and apostrophes, you have a problem in the video RAM or associated circuitry.

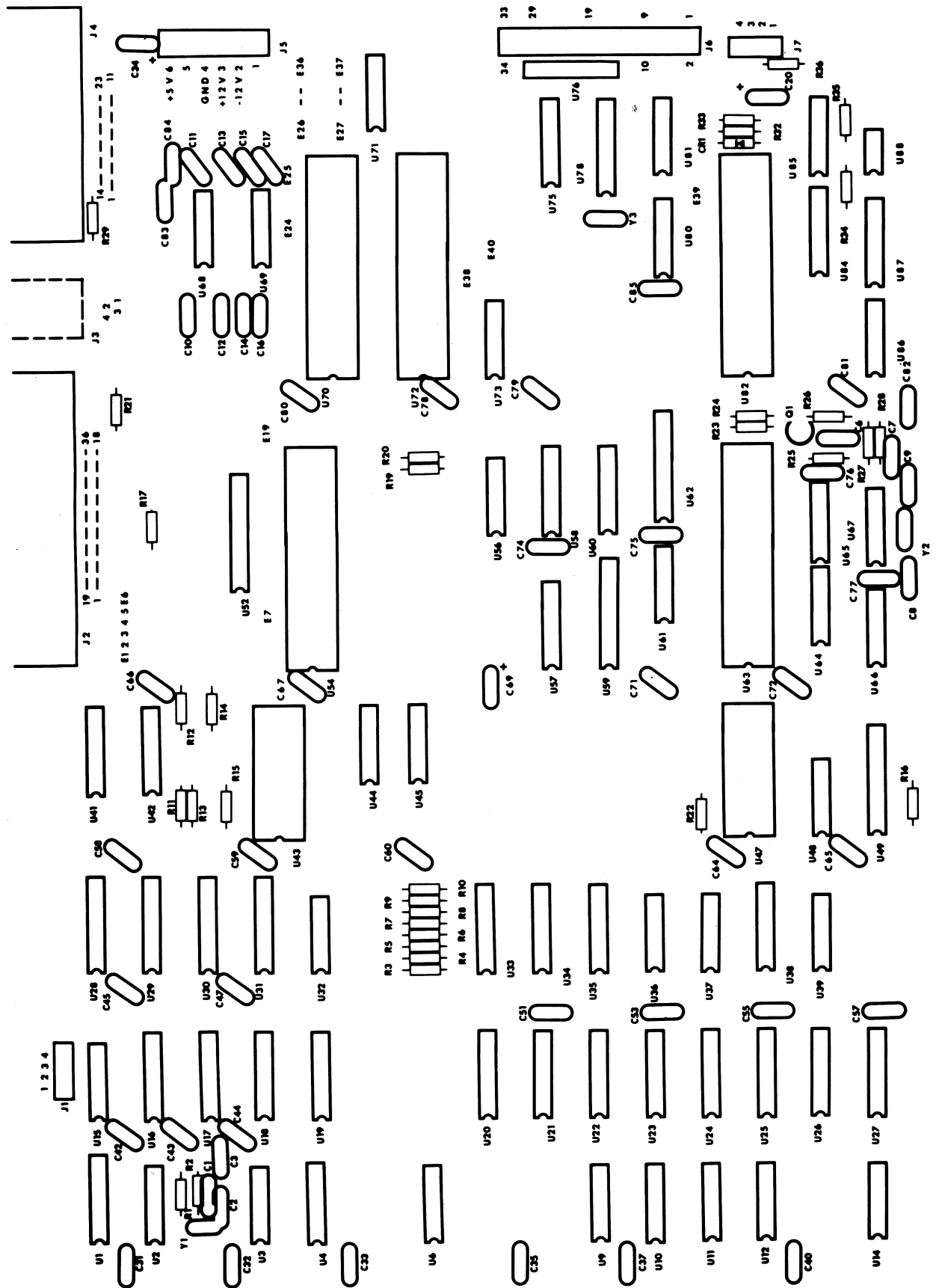
You can check the main RAM and associated circuitry quickly with a scope by looking at pin 14 on each of the RAM chips. The following pattern should be observed:

For the 81-240 board:

U20 (D7)	low
U21 (D6)	low
U22 (D5)	toggle
U23 (D4)	toggle
U24 (D3)	toggle
U25 (D2)	low
U26 (D1)	low
U27 (D0)	toggle

The U-numbers of the main RAM chips will vary depending on which mainboard you have, but the pattern will be the same.

6.1 KAYPRO 2 (81-110-n)



IC LIST, KAYPRO 2 (81-110-n)

Reference Designation		Description
U1	74LS161	4-bit counter
U2, U67*	74HCU04	Hex inverter, CMOS
U3	74LS290	decade counter
U4	74LS10	Tri NAND gates
U6, U11	74LS393	Dual binary counter
U9, U80	74LS08	Quad AND gates
U10, U61	73LS32	Quad OR Gates
U12, U14, U32	74LS74	Dual "D" flip-flop
U15, U39	74LS00	Quad NAND Gates
U16 through U19	74LS157	Quad 2/1 MUX
U20 through U27	MCM6665	(or equivalent) 64K x 1 RAM
U28 through U31	2114	1K x 4 RAM
U33, U34	74157	Quad 2/1 MUX
U35, U38	8216	Quad Bi-directional MUX
U36	74LS20	Dual NAND gates
U37, U56, U85	74LS02	Quad NOR gates
U41	74S151	8/1 MUX
U42	74LS174	Hex "D" flip-flop
U43	81-146	Character generator EPROM
U44, U45, U64, U65	74LS243	Quad bus trans
U47	81-149	Boot EPROM
U48, U73	74LS04	Hex inverter
U49, U52, U62	74LS241	Octal buffer
U54, U72	Z80 PIO	
U57, U58, U60	74LS138	3/8 MUX
U59	74LS373	Octal "D" latch
U63	Z80 CPU	
U66	74164	8-bit shift register
U68	1488	Quad line driver (OUT)
U69	1489	Quad SCHMITT line receiver (IN)
U70	Z80 SIO	
U71	74S04	Hex inverter
U78	8116	Dual programmable baud rate generator
U81	7406	Hex inverter, open collector
U82	FD1793	Floppy disk controller
U84	74LS195	4-bit shift register
U86	74LS293	4-bit binary counter
U87	74LS390	Dual decade counter
U88	FDC9216	Data separator

*NOTE: THERE ARE SOME VERSIONS OF THE 81-110 BOARD ON WHICH U2 AND U67 ARE NOT CMOS IC.S BUT ARE NORMAL TTL IC.S. READ THE NUMBER ON THE IC TO BE SURE.

SCOPE SIGNALS TO AID IN TROUBLESHOOTING (81-110-n)

The examples of correct signals shown below do not represent all of the signals on a Kaypro mainboard, since most signals will be simple high-low toggles. A group of video signals (CC0 through CC3) are included as illustrations of the timing relationships between the various video signals. Note that only one of the I/O signals on U57 will be low at any given time.

State of the machine: The door of drive A is open; the machine is waiting to boot.

The signal measurements were made using a Tektronix oscilloscope, model 2213. It has a bandwidth of DC-60 MHz, sensitivity of 2mV/cm, a sweep delay of 0.1 microseconds to 1 second, and a graticule display measuring 8 x 10 cm.

Signal M1 from pin 27 of the CPU (U63) was triggered on. This signal is shown in the top half of each display and was channel 1. Ground for the signals shown on channel 2 was established at first graticule line above the bottom of the display.

Each square of the representation is the equivalent of one square cm on the graticule. The scope was set for 2V/div. for all figures, and was set for .5 micro-secs/div. for all figures EXCEPT figure 6, which was taken using 1 micro-secs/div.

Figure 1: Pin 6 of U63, 2.5MHz clock signal.

Figure 2: Pin 24 of U82, 1MHz clock signal.

Figure 3: Pin 3 of U6, CC0.

Figure 4: Pin 4 of U6, CC1.

Figure 5: Pin 5 of U6, CC2.

Figure 6: Pin 6 of U6, CC3.

FIGURE 1

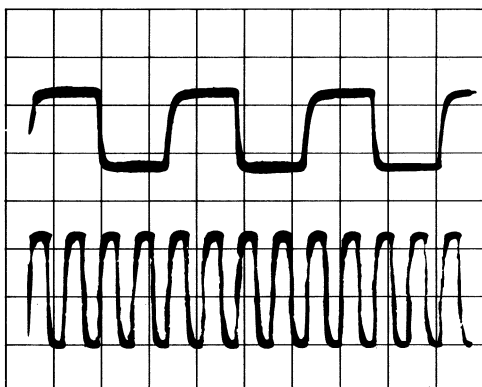


FIGURE 2

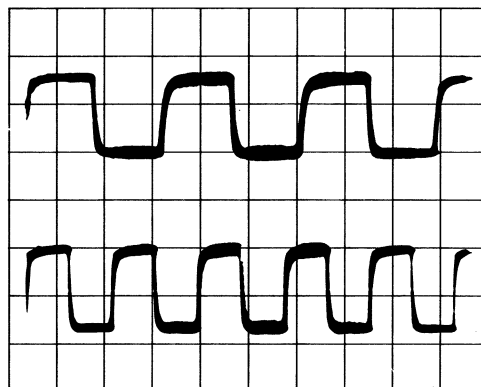


FIGURE 3

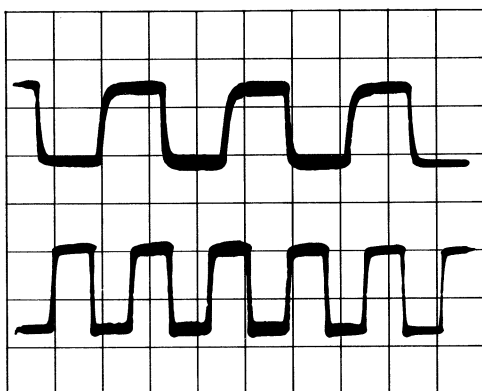


FIGURE 4

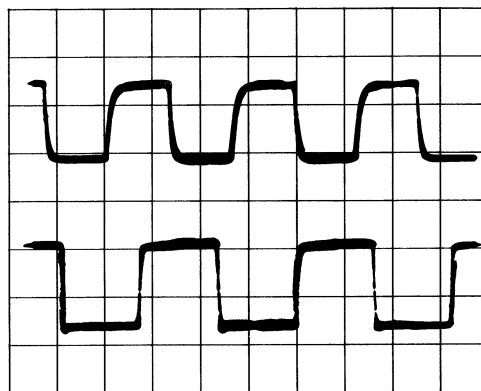


FIGURE 5

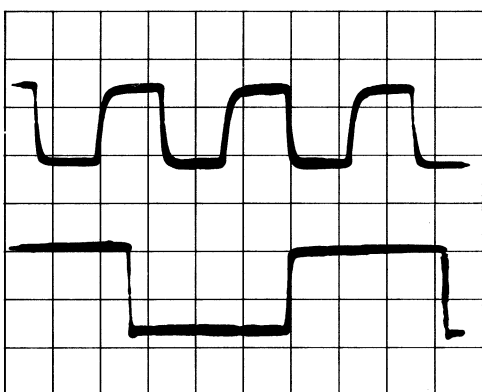
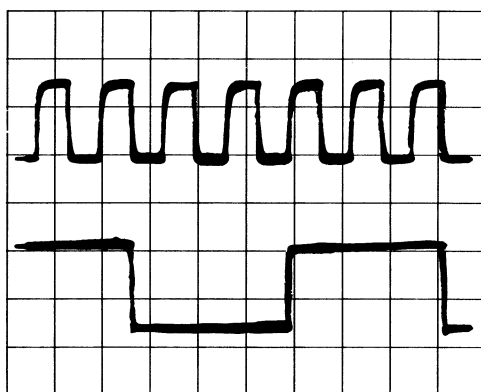
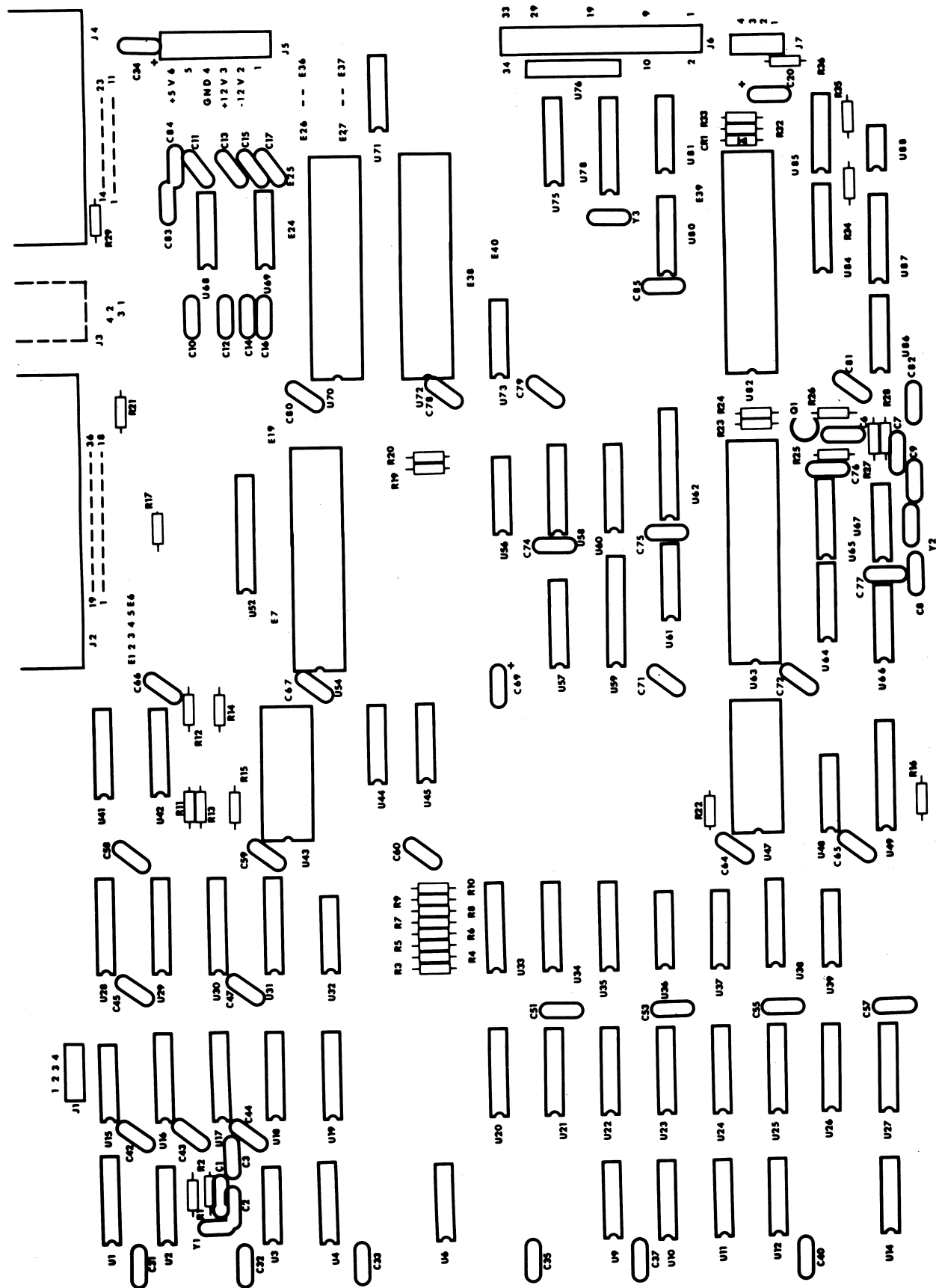


FIGURE 6



SIGNAL LOCATIONS ON MAINBOARD 81-110-n

<u>SIGNAL</u>	<u>IC</u> <u>LOCATION</u>	<u>PIN NO.</u>
CPU SIGNALS		
2.5 MHz	U 63	6
MREQB	U 62	16
RDB	U 62	5
WRB	U 62	14
MEMORY SIGNALS		
RAS	U 39	11
CAS	U 66	5
MUXC	U 66	4
VIDEO CLOCK SIGNALS		
CLOCK, Y1	U 2	8
CC0	U 6	3
CC1	U 6	4
CC2	U 6	5
CC3	U 6	6



IC LIST, KAYPRO 2/4 (81-240-n)

Reference Designation		Description
U1	74LS161	4-bit counter
U2, U67	74HCU04	Hex inverter, CMOS
U3	74LS290	Decade counter
U4	74LS10	Tri NAND gates
U6, U11	74LS393	Dual binary counter
U9, U80	74LS08	Quad AND gates
U10, U61	73LS32	Quad OR Gates
U12, U14, U32	74LS74	Dual "D" flip-flop
U15, U39	74LS00	Quad NAND Gates
U16 through U19	74LS157	Quad 2/1 MUX
U20 through U27	MCM6665	(or equivalent) 64K x 1 RAM
U28 through U31	2114	1K x 4 RAM
U33, U34	74157	Quad 2/1 MUX
U35, U38	8216	Quad Bi-directional MUX
U36	74LS20	Dual NAND gates
U37, U56, U85	74LS02	Quad NOR gates
U41	74S151	8/1 MUX
U42	74LS174	Hex "D" flip-flop
U43	81-146-n	Character generator EPROM
U44, U45, U64, U65	74LS243	Quad bus trans
U47	81-232-n	Boot EPROM
U48, U73	74LS04	Hex inverter
U49, U52, U62	74LS241	Octal buffer
U54, U72	Z80 PIO	
U57, U58, U60	74LS138	3/8 MUX
U59	74LS373	Octal "D" latch
U63	Z80 CPU	
U66	74164	8-bit shift register
U68	MC1488	Quad line driver (OUT)
U69	MC1489	Quad SCHMITT line receiver (IN)
U70	Z80 SIO	
U71	74S04	Hex inverter
U78	8116	Dual programmable baud rate generator
U81	7406	Hex inverter, open collector
U82	1793	Floppy disk controller
U84	74LS195	4-bit shift register
U86	74LS293	4-bit binary counter
U87	74LS390	Dual decade counter
U88	FDC9216	Data separator

SCOPE SIGNALS FOR TROUBLESHOOTING 81-240-n

The examples of correct signals shown below do not represent all of the signals on a Kaypro mainboard, since most signals will be simple high-low toggles. A group of video signals (CC0 through CC3) are included as illustrations of the timing relationships between the various video signals. Note that only one of the I/O signals on U57 will be low at any given time.

State of the machine: with a blank, 2732, EPROM inserted in place of normal EPROM at location U 47. The door of drive A is open; the machine is waiting to boot.

The signal measurements were made using a Tektronix oscilloscope, model 2213. It has a bandwidth of DC-60 MHz, sensitivity of 2mV/cm, a sweep delay of 0.1 microseconds to 1 second, and a graticule display measuring 8 x 10 cm.

Signal M1 from pin 27 of the CPU (U63) was triggered on. This signal is shown in the top half of each display and was channel 1. Ground for the signals shown on channel 2 was established at first graticule line above the bottom of the display.

Each square of the representation is the equivalent of one square cm on the graticule. The scope was set for 2V/div. for all figures, and was set for .5 micro-secs/div. for all figures EXCEPT figure 6, which was taken using 1 micro-secs/div.

Figure 1: Pin 6 of U63, 2.5MHz clock signal.

Figure 2: Pin 24 of U82, 1MHz clock signal.

Figure 3: Pin 3 of U6, CC0.

Figure 4: Pin 4 of U6, CC1.

Figure 5: Pin 5 of U6, CC2.

Figure 6: Pin 6 of U6, CC3.

FIGURE 1

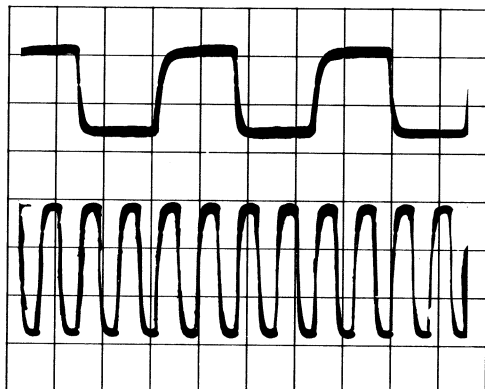


FIGURE 2

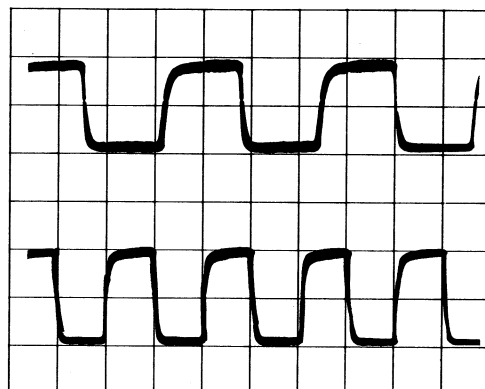


FIGURE 3

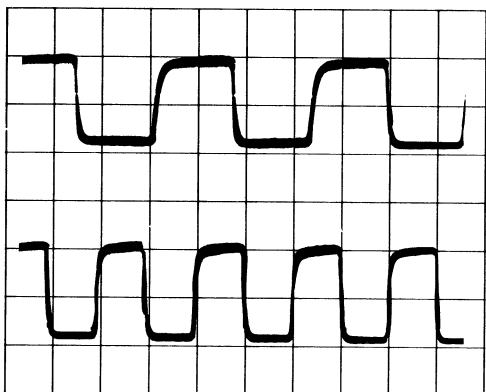


FIGURE 4

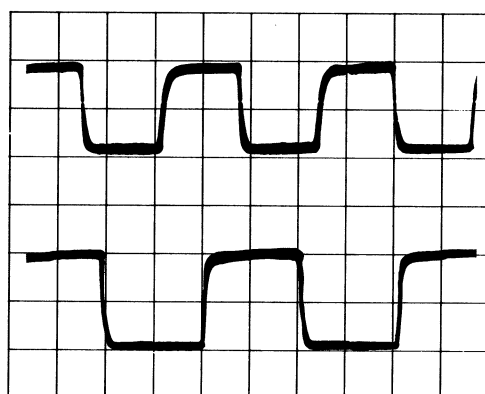


FIGURE 5

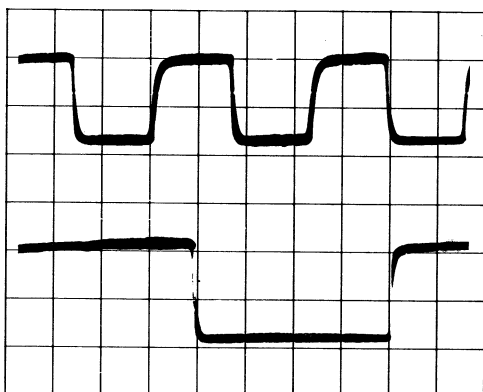
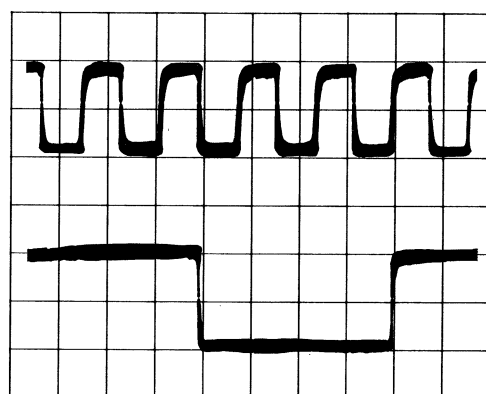


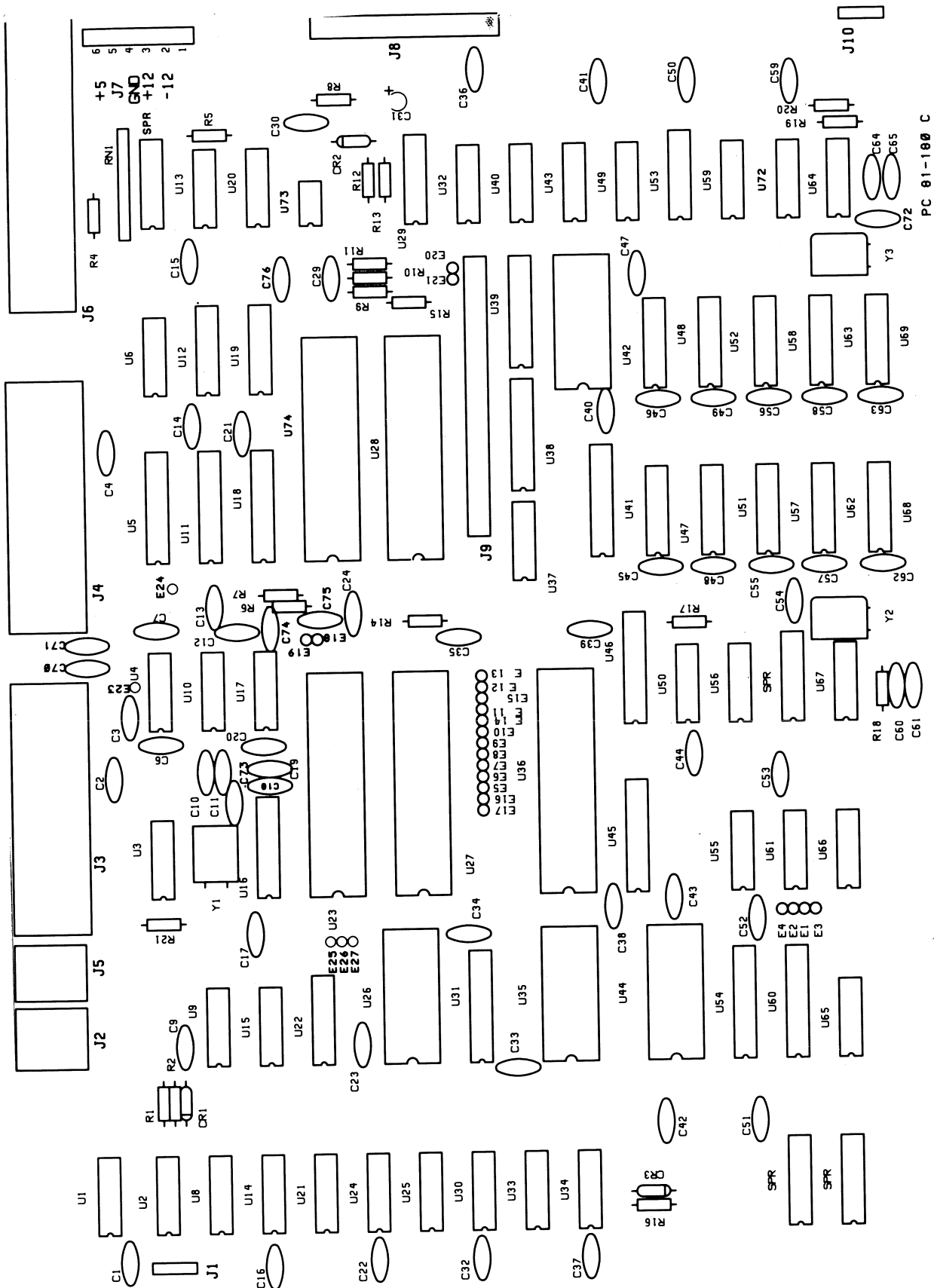
FIGURE 6



SIGNAL LOCATIONS, 81-240-n

<u>SIGNAL</u>	<u>IC</u> <u>LOCATION</u>	<u>PIN NO.</u>
CPU SIGNALS		
CPU clock	U 67	6
1 MHz	U 87	13
2 MHz	U 87	3
MREQB	U 62	16
RDB	U 62	5
MIR (reference)		
MEMORY SIGNALS		
RAS	U 39	11
CAS	U 66	5
MUXC	U 66	4
VIDEO CLOCK SIGNALS		
CLOCK, Y1	U 2	8
CC0	U 6	3
CC1	U 6	4
CC2	U 6	5
CC3	U 6	6

6.3 KAYPRO 10 MAINBOARD (81-180-n)



IC LIST, 81-180-n

Reference Designation		Description
U1	74S74	Dual "D" flip-flop
U2	74S08	Hex Schmitt Quad AND gate
U3	74S04	Hex inverter
U4, U10	1489	Quad SCHMITT line receiver
U5, U38, U41	74LS244	Octal buffer
U6, U21, U24, U33, U49	74LS74	Dual "D" flip-flop
U20, U30	74LS08	Quad AND gate
U8, U37, U50	74LS00	Quad NAND gate
U9, U59	74LS393	Dual binary counter
U11, U18, U31, U45, U46, U60	74LS373	Octal "D" latch
U12, U19	74LS138	3/8 MUX
U13	7406	Hex inverter, open collector
U14	74S86	Hex Schmitt Quad XOR gate
U15, U61	74S20	Dual NAND gate
U16	8116	Dual programmable baud rate generator
U17	1488	Quad transmitter
U22	74LS165	8-bit shift register
U23, U27	Z80A SIO	
U25, U72	74LS10	Tri NAND gate
U26	81-187	Character generator EPROM (2732)
U28	Z80A CPU	
U29	74LS195	4-bit shift register
U32, U43	74LS02	Quad NOR gate
U34	74LS14	Hex Schmitt inverter
U35, U44	6116	RAM, 2K x 8-bit (200ns)
U36	6545A-1	Video controller
U39, U54	74LS245	Bi-directional 8-bit buffer
U42	81-302-C	Boot EPROM (2732)
U40	74LS32	Quad OR gate
U47, U48	74LS157	Quad 2/1 MUX
U51, U52, U57, U58, U62, U63, U68, U69	4565N-15	Dynamic RAM 64K x 1 (150ns) (Mostek number)
U53	74LS163	Synchronous 4-bit binary counter
U55	14-2-392	14-Pin pull-up resistor
U56, U65	74LS04	Hex inverter
U64, U67	74HCU04	Hex inverter, CMOS
U66	74LS93	Asynchronous 4-bit binary counter
U73	WD9216	Data separator
U74	1793	Floppy disk controller

SCOPE SIGNALS FOR TROUBLESHOOTING 81-180-n

The examples of correct signals shown here do not represent all of the signals on the 81-180-n mainboard, since most of the signals will be high-low toggles. A group of signals taken from a video clock divider (U66) are shown to illustrate the timing relationships between the various video signals.

The signal measurements were made using a Tektronix oscilloscope, model 2213. It has a bandwidth of DC-60 MHz, sensitivity of 2mV/cm, a sweep delay of 0.1 microseconds to 1 second, and a graticule display measuring 8 x 10 cm.

Each square of the representation is the equivalent of one square cm. on the graticule. The scope was set for 2V/div. and .2micro-secs/div. for all figures except figure 2, which was set at .5micro-secs/div.

Signal M1 from pin 27 of the CPU (U28) was triggered on. This signal is shown in the top half of each display and was channel 1. Ground for signals shown on channel 2 was established at the first graticule line above the bottom of the display.

State of the machine: The machine has just booted-up on the hard drive.

Figure 1: Pin 6 of U28, 4MHz clock signal.

Figure 2: Pin 24 of U74, 1MHz clock signal.

Figure 3: Pin 12 of U66, video, clock divider.

Figure 4: Pin 9 of U66, video, clock divider.

Figure 5: Pin 8 of U66, video, clock divider.

Figure 6: Pin 11 of U66, video, clock divider.

FIGURE 1

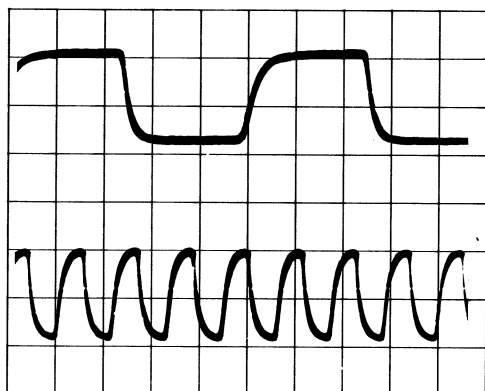


FIGURE 2

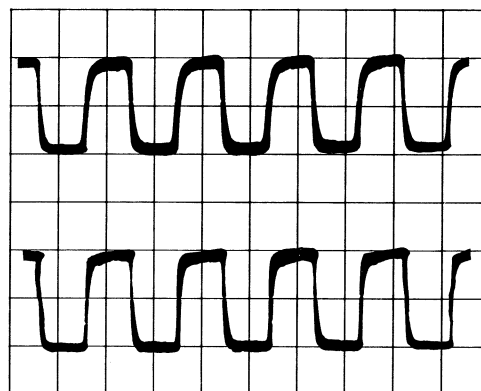


FIGURE 3

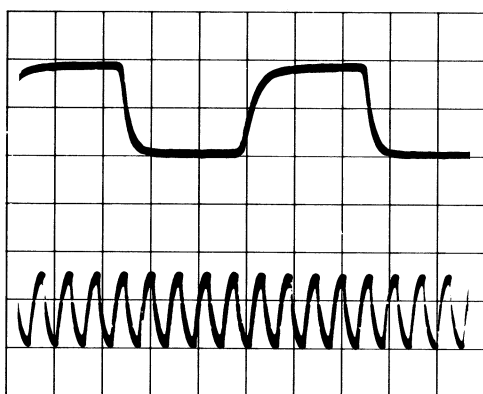


FIGURE 4

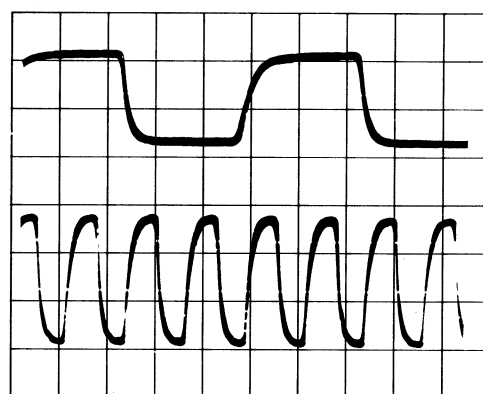


FIGURE 5

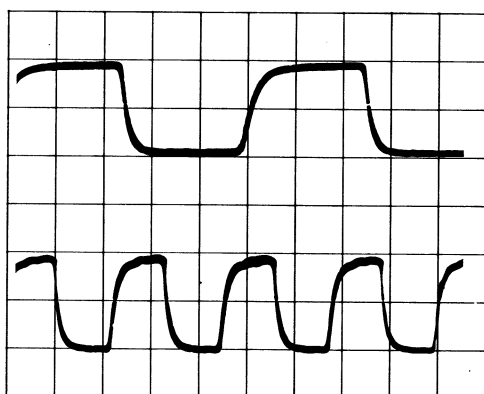
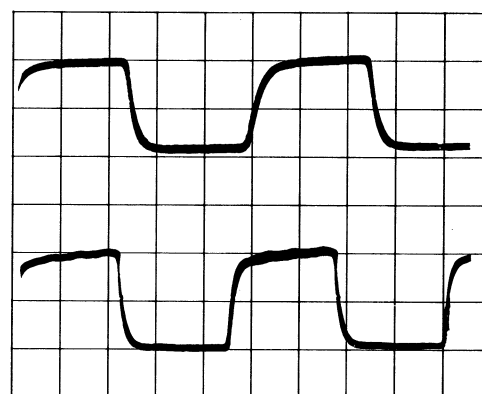
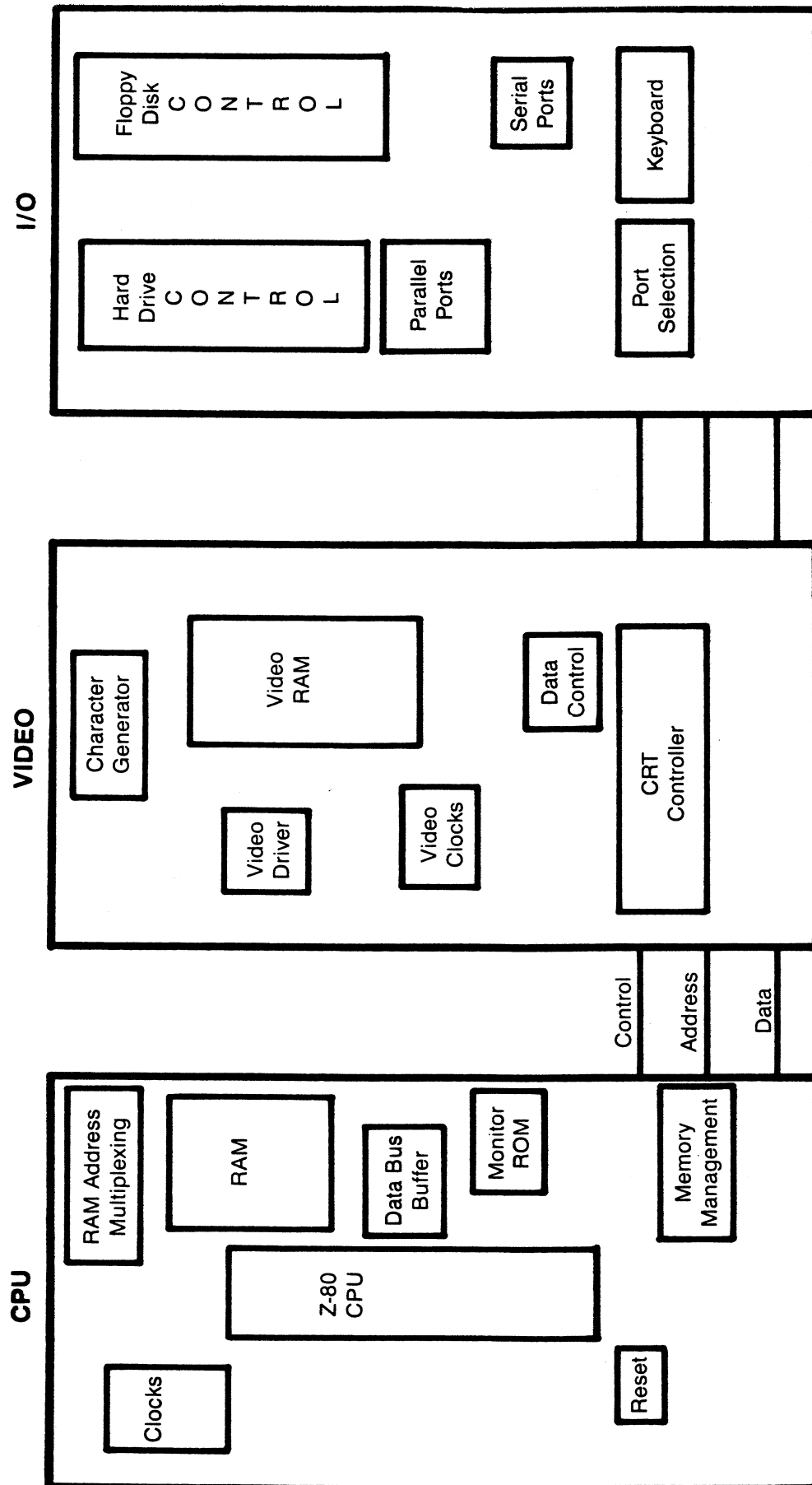


FIGURE 6





KAYPRO 10 BLOCK DIAGRAM

6.4 KAYPRO 2/84 AND 2X (81-294-n)



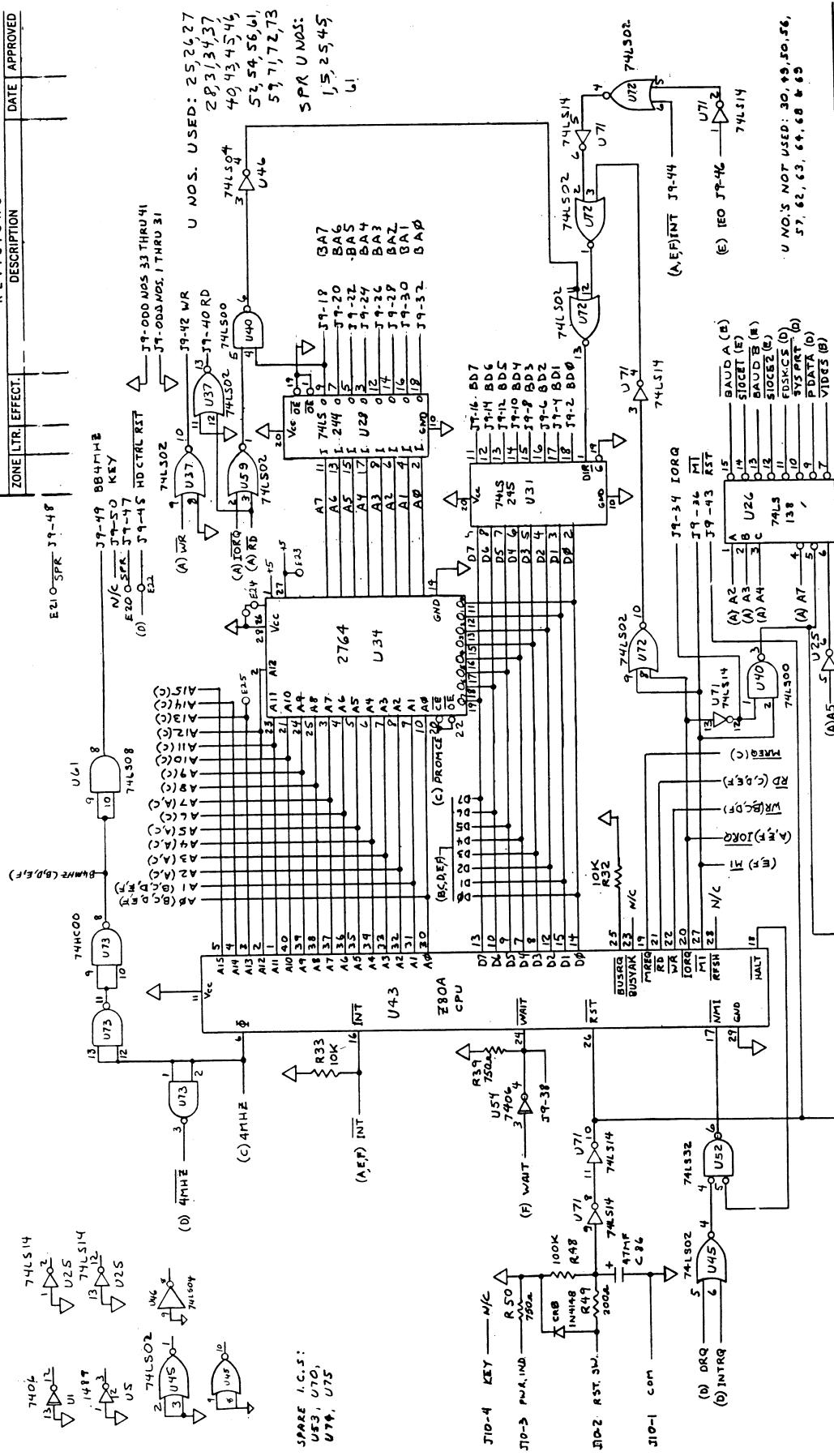
MADE IN U. S. A.

ASSY 81- -

IC LIST, 81-294-n

Reference Designation		Description
U1, U54	7406	Hex inverter, open collector
U2, U25, U71	74LS14	Hex Schmitt inverter
U3, U5	1489	Quad Schmitt line driver
U4	1488	Quad line driver
U6, U13, U14, U55, U58	74LS373	Octal "D" latch
U8	74LS374	Octal "D" flip-flop
U9	81-235-n	Character PROM (2732)
U10	81-189	Custom gate array
U11, U17	Z80 SIO	
U15, U23	6116	Video RAM
U16	6545EA	CRT controller
U20, U31	74LS245	Octal bus transceiver
U22	3.9K	Pull-up Resistor
U24	WD1943/ 8116	Dual programmable baud rate generator
U26, U27	74LS138	3/8 MUX
U28, U51	74LS244	Octal buffer/line driver (3-state outputs)
U29	81-194	Custom gate array
U32, U33, U38, U39, U41, U42, U47, U48	2164	64K x 1 RAM
U34	81-292	Boot PROM (2764)
U37, U45, U59, U72	74LS02	Quad NOR gate
U40	74LS00	Quad NAND gate
U43	Z80A CPU	
U44	1793	Floppy disk controller
U46	74LS04	Hex inverter
U52	74LS32	Quad OR gate
U60	74LS195	4-bit shift register
U61	74LS08	Quad AND gate
U65	74LS10	Tri NAND gate
U66, U75	74LS74	Dual "D" flip-flop
U67	FDC9216	Data separator
U73	74HC00	Quad NAND gate

REVISIONS	DESCRIPTION	DATE	APPROVED
ZONE LTR. EFFECT.			



U NOS. USED: 25, 26, 27, 28, 31, 34, 37, 40, 43, 45, 46, 52, 54, 56, 61, 57, 71, 72, 73, 58, 25, 45, 61

SPR UNOS: 1, 5, 25, 45, 61

U NOS. NOT USED: 30, 39, 50, 56, 57, 62, 63, 64, 68 & 69

NON-LINEAR SYSTEMS, INC.
DEL MAR, CALIFORNIA

SCHEMATIC, PROCESSOR

KAYPRO 2/84

SIZE CODE IDENT NO. C 03626

81-275

SCALE WEIGHT SHEET 2A

QTY RECD PART OR IDENTIFYING NO. NOMENCLATURE OR DESCRIPTION PARTS LIST MFR. CODE CIRCUIT ITEM NO.

UNLESS OTHERWISE SPECIFIED NO. CONTR. NO. DIMENSIONS ARE IN INCHES TOLERANCES ON DRAWN CHECK R 3 2-29-74 RELEASE DATE 2-6-84 KSI BOARD REV A

DECIMALS ANGLES ±0.005 FRACTIONS ±1/32 MATERIAL

PROPRIETARY INFORMATION

Not for distribution or transmission to any other person or company without the permission of Kaypro Corporation

SPARE J7-1 TO: U4-1

-12V J7-2 TO: U4-1

+12V J7-3 TO: U4-1, U44-40

GND J7-4

KEY J7-5

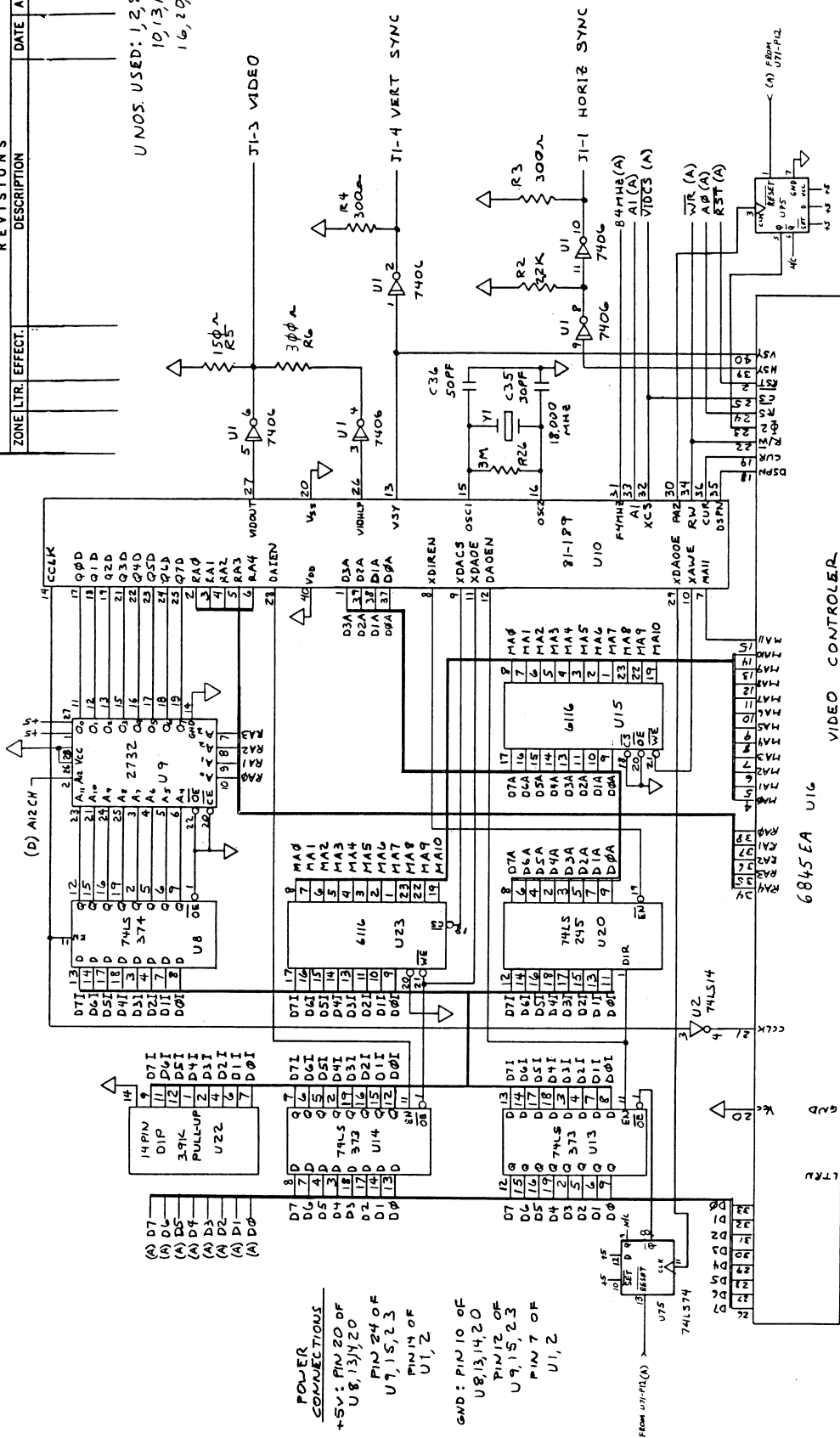
+5V J7-6

APPLICATION QTY.

NEXT ASSY. FINAL ASSY. NEXT FINAL ASSY.

ZONE	LTR.	EFFECT.	REVISIONS	DESCRIPTION	DATE	APPROVED

UNOS. USED: 1,2,8,9,
10,13,14,15,
16,20,22,23



QTY.	REQD.	PART OR IDENTIFYING NO.	NOMENCLATURE OR DESCRIPTION	MFR. CODE	CIRCUIT REF. NO.

UNLESS OTHERWISE SPECIFIED		CONTR. NO.	
DRAWN	2-29-87	CHECK	2-29-87
APPR	2-29-87	APPR	2-29-87
RELEASE DATE 3-6-84 KSI		BOARD REV A	

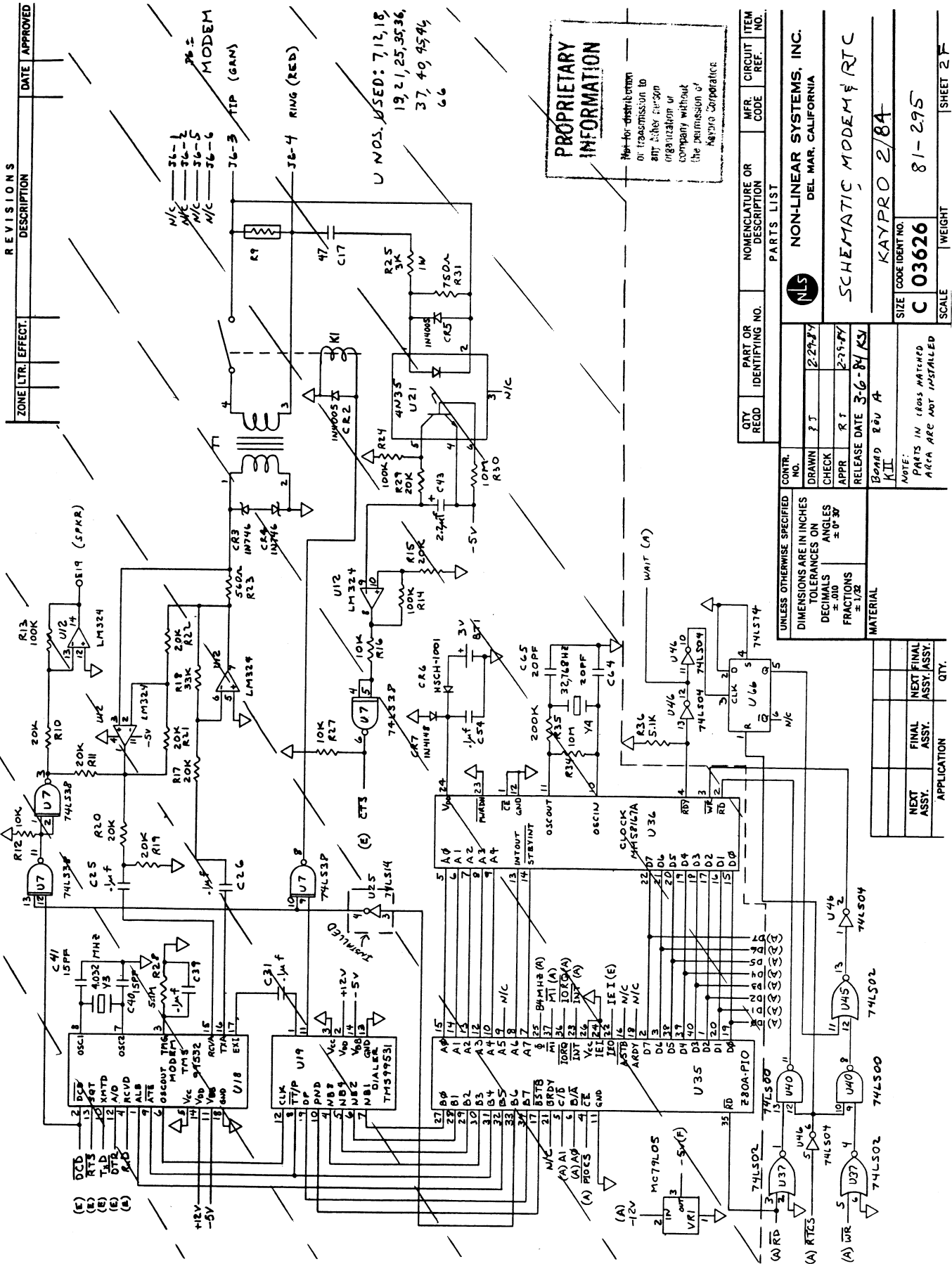
DIMENSIONS ARE IN INCHES		TOLERANCES ON ANGLES	
DECIMALS	± .010	DECIMALS	± .010
FRACTIONS	± 1/32	FRACTIONS	± 1/32

APPLICATION	QTY.
NEXT ASST.	
FINAL ASST.	
NEXT FINAL ASST.	

PROPRIETARY INFORMATION	
Not for distribution outside of the company without the permission of the company.	

NON-LINEAR SYSTEMS, INC.	
DEL MAR, CALIFORNIA	
SCHEMATIC, VIDEO	
KAYPRO 2/84	
SIZE	CODE IDENT NO.
C 03626	81-295
SCALE	WEIGHT
	SHEET 28

REVISIONS	DATE	APPROVED
ZONE LTR. EFFECT.	DESCRIPTION	



PROPRIETARY INFORMATION

Not for distribution or transmission to any other person or organization without the permission of the company.

QTY REQD	IDENTIFYING NO.	NOMENCLATURE OR DESCRIPTION	MFR. CODE	CIRCUIT REF.	ITEM NO.
1	2	3	4	5	6
7	8	9	10	11	12
13	14	15	16	17	18
19	20	21	22	23	24
25	26	27	28	29	30
31	32	33	34	35	36
37	38	39	40	41	42
43	44	45	46	47	48
49	50	51	52	53	54
55	56	57	58	59	60
61	62	63	64	65	66
67	68	69	70	71	72
73	74	75	76	77	78
79	80	81	82	83	84
85	86	87	88	89	90
91	92	93	94	95	96
97	98	99	100	101	102

NON-LINEAR SYSTEMS, INC.

DEL MAR, CALIFORNIA

SCHEMATIC MODEM & RTC

KAYPRO 2/84

81-295

C 03626

RELEASE DATE 3-6-84

BOARD 220 A

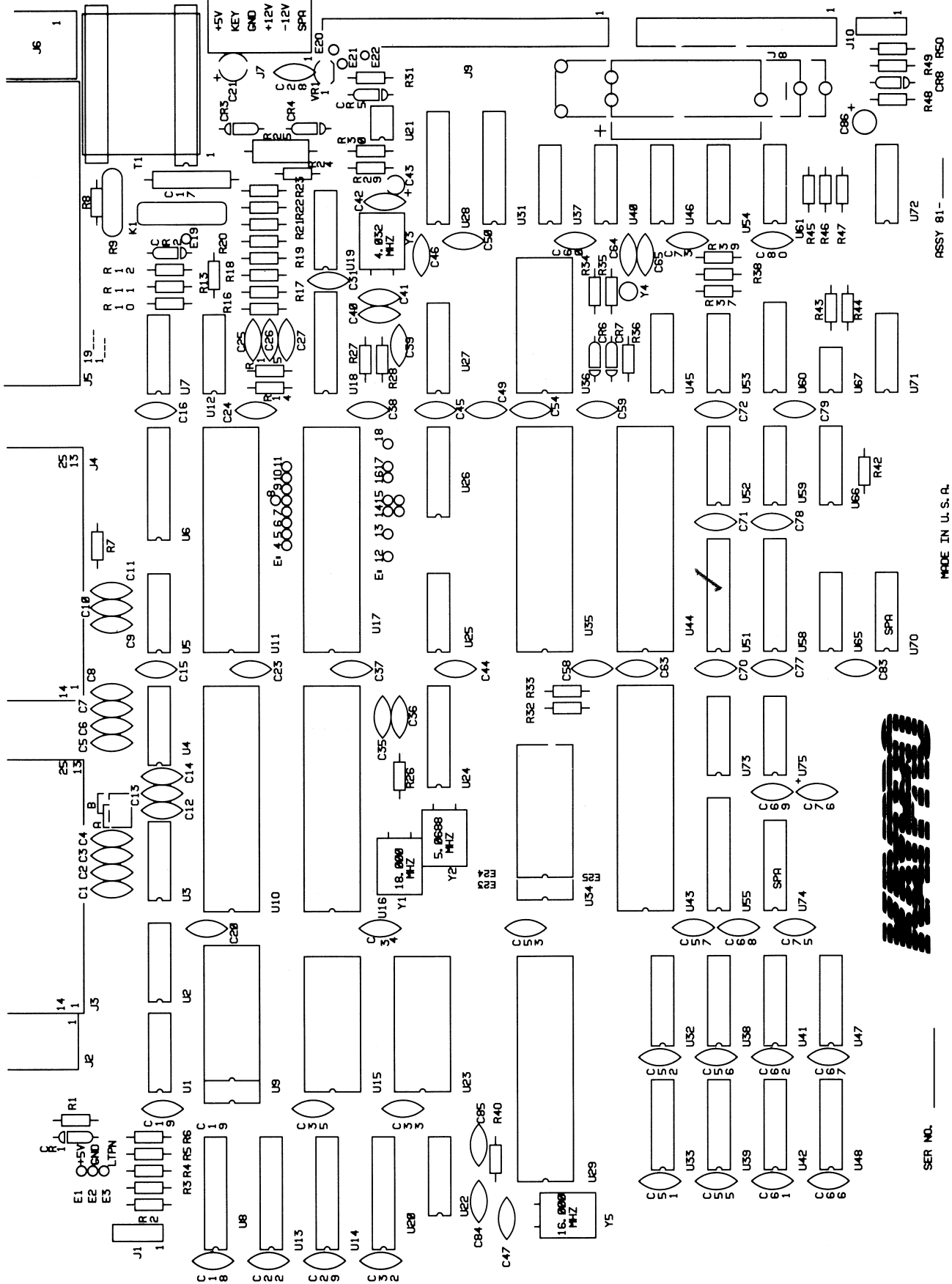
NOTE: PARTS IN 100% MATCHED AREA ARE NOT INSTALLED

SCALE

WEIGHT

SHEET 2 F

6.5 KAYPRO 4/84 MAINBOARD (81-184-n)



KAYPRO

SER. NO. _____

MADE IN U. S. A.

ASSY 81- _____

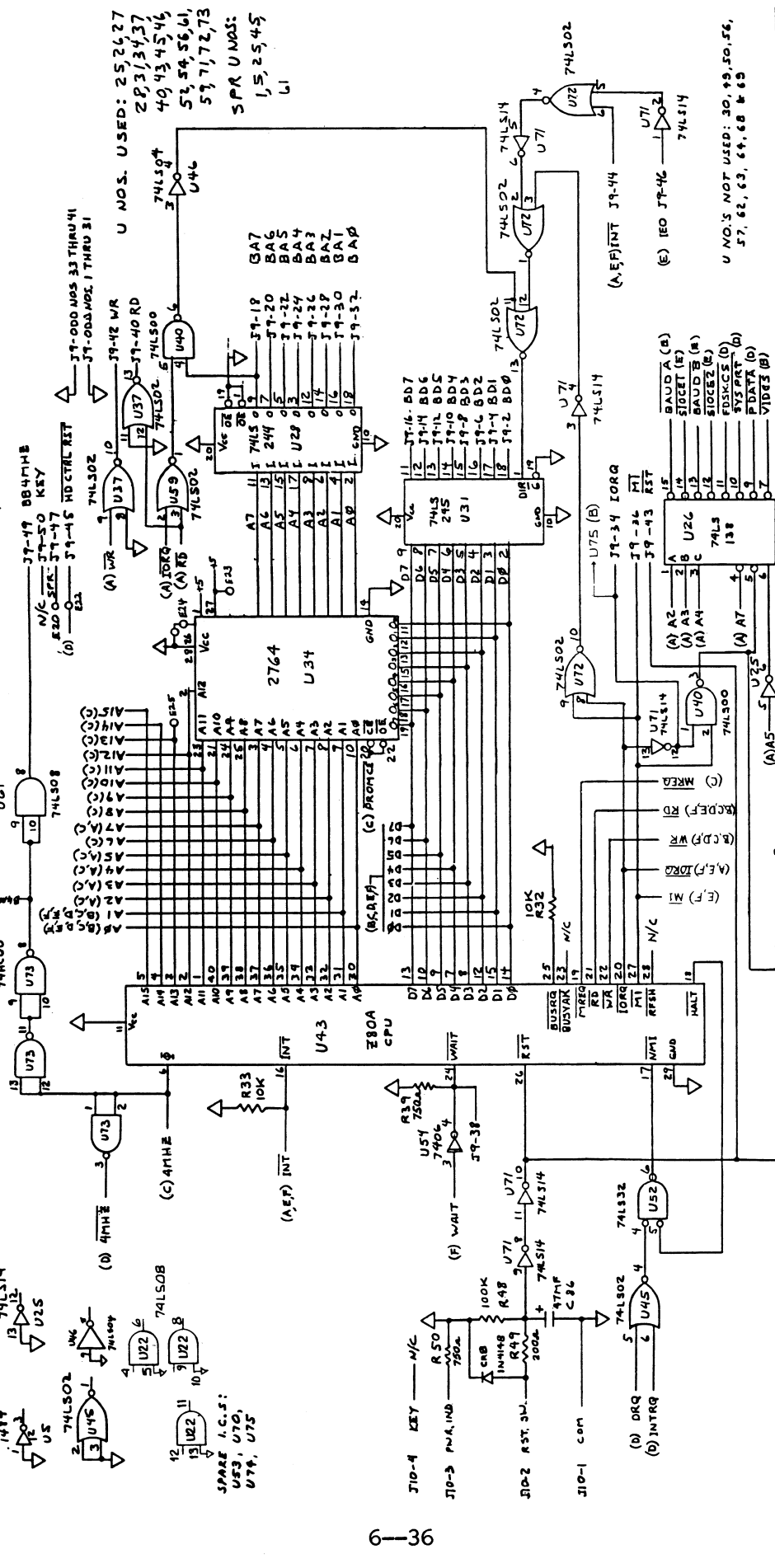
IC LIST, 81-184-n

Reference Designation		Description
U1, U54	7406	Hex inverter, open collector
U2, U25, U71	74LS14	Hex Schmitt inverter
U3, U5	1489	Quad Schmitt line driver
U4	1488	Quad line driver
U6, U13, U14, U55, U58	74LS373	Octal "D" latch
U7	74LS38	Positive-NAND buffer
U8	74LS374	Octal "D" flip-flop
U9	81-235	Character PROM (2732)
U10	81-189	Custom gate array
U11, U17	Z80A SIO	
U12	LM324	Op-Amp
U15, U23	6116	Video RAM
U16	6545A	Video controller
U18	TMS 99532	Modem
U19	TMS 99531	Dialer
U20, U31	74LS245	Octal bus transceiver (3-state outputs)
U21	4N35	Photo isolator
U22	3.9K	Pullup resistor, 14-pin DIP
U24	WD1943/ 8116	Dual programmable baud rate generator
U26, U27	74LS138	3/8 MUX
U28, U51	74LS244	Octal buffer/line driver
U29	81-194	Custom gate array
U32, U33, U38, U39, U41, U42, U47, U48	2164	64K x 1 RAM
U34	81-292	Boot PROM (2764)
U35	Z80A PIO	
U36	MM581678	Clock
U37, U45, U59, U72	74LS02	Quad NOR gate
U40	74LS00	Quad NAND gate
U43	Z80A CPU	
<u>U44</u>	<u>1793</u>	<u>Floppy disk controller</u>
U46	74LS04	Hex inverter
U52	74LS32	Quad OR gate
U53, U70, U74, U75		Spares
U60	74LS195	4-bit shift register
U61	74LS08	Quad AND gate
U65	74LS10	Tri NAND gate
U66, U75	74LS74	Dual "D" flip-flop
U67	FDC9216	Data separator
U73	74HC00	Quad NAND gate

REVISIONS			DATE	APPROVED
ZONE	LTR.	EFFECT.		
A	11-8-83	REMOVED U73 (74508)	11-10-83	KWJ
B	ALL	ADDED U73	11-15-83	RWP

ZONE	LTR.	EFFECT.	DATE	APPROVED
A	11-8-83	REMOVED U73 (74508)	11-10-83	KWJ
B	ALL	ADDED U73	11-15-83	RWP

ZONE	LTR.	EFFECT.	DATE	APPROVED
A	11-8-83	REMOVED U73 (74508)	11-10-83	KWJ
B	ALL	ADDED U73	11-15-83	RWP



QTY REQD		PART OR IDENTIFYING NO.		NOMENCLATURE OR DESCRIPTION		MFR. CODE		CIRCUIT REF.		ITEM NO.	
PARTS LIST											
74LS14											
UNLESS OTHERWISE SPECIFIED											
DIMENSIONS ARE IN INCHES											
TOLERANCES ON DECIMALS											
FRACTIONS											
MATERIAL											
BOARD 2xv B											
(81-184)											
SIZE CODE IDENT NO.											
C 03626											
SCALE WEIGHT SHEET 2A											
NON-LINEAR SYSTEMS, INC.											
DEL MAR, CALIFORNIA											
SCHEMATIC, PROCESSOR											
KAYPRO 4/84											
81-185											

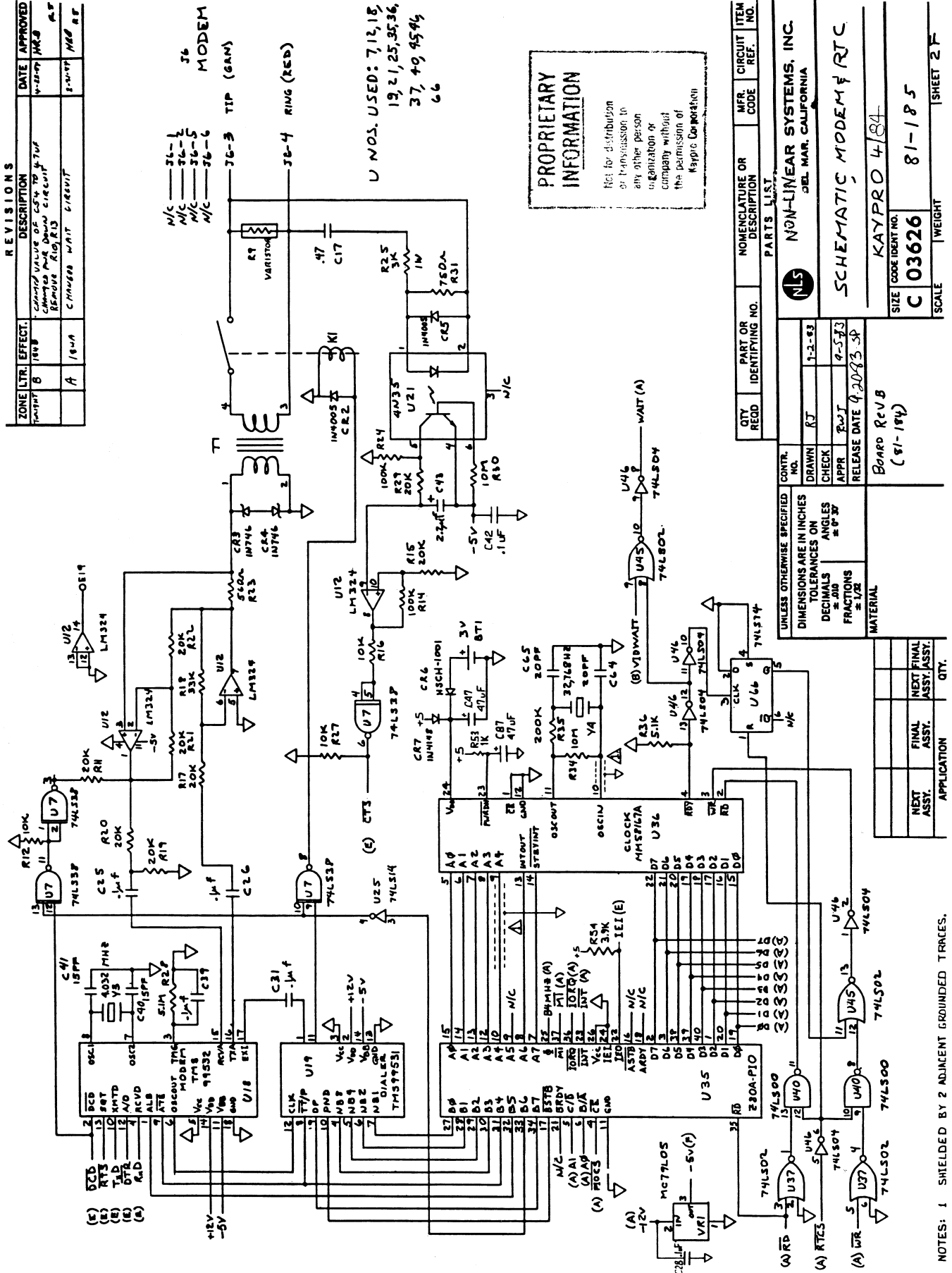
PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P)		PLOTS (P	
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SPARE 37-1	TO: U4-1
-12V 37-2	TO: U4-19, U44-40
+12V 37-3	TO: U4-19, U44-40
GND 37-4	TO: U4-19, U44-40
KEY 37-5	TO: U4-19, U44-40
+5V 37-6	TO: U4-19, U44-40

PROPRIETARY
 INFORMATION
 ALL INFORMATION CONTAINED HEREIN IS UNCLASSIFIED
 DATE 11-10-83 BY 11-10-83

REVISIONS

ZONE	LTR.	EFFECT.	DESCRIPTION	DATE	APPROVED
1	8	1848	CHANGED VALUE OF C54 TO 4.7UF CHANGED AND DOWN CIRCUIT REMOVED R10, R13	4-21-79	MAJ
2	A	1848	CHANGED WAIT CIRCUIT	2-20-79	MAJ



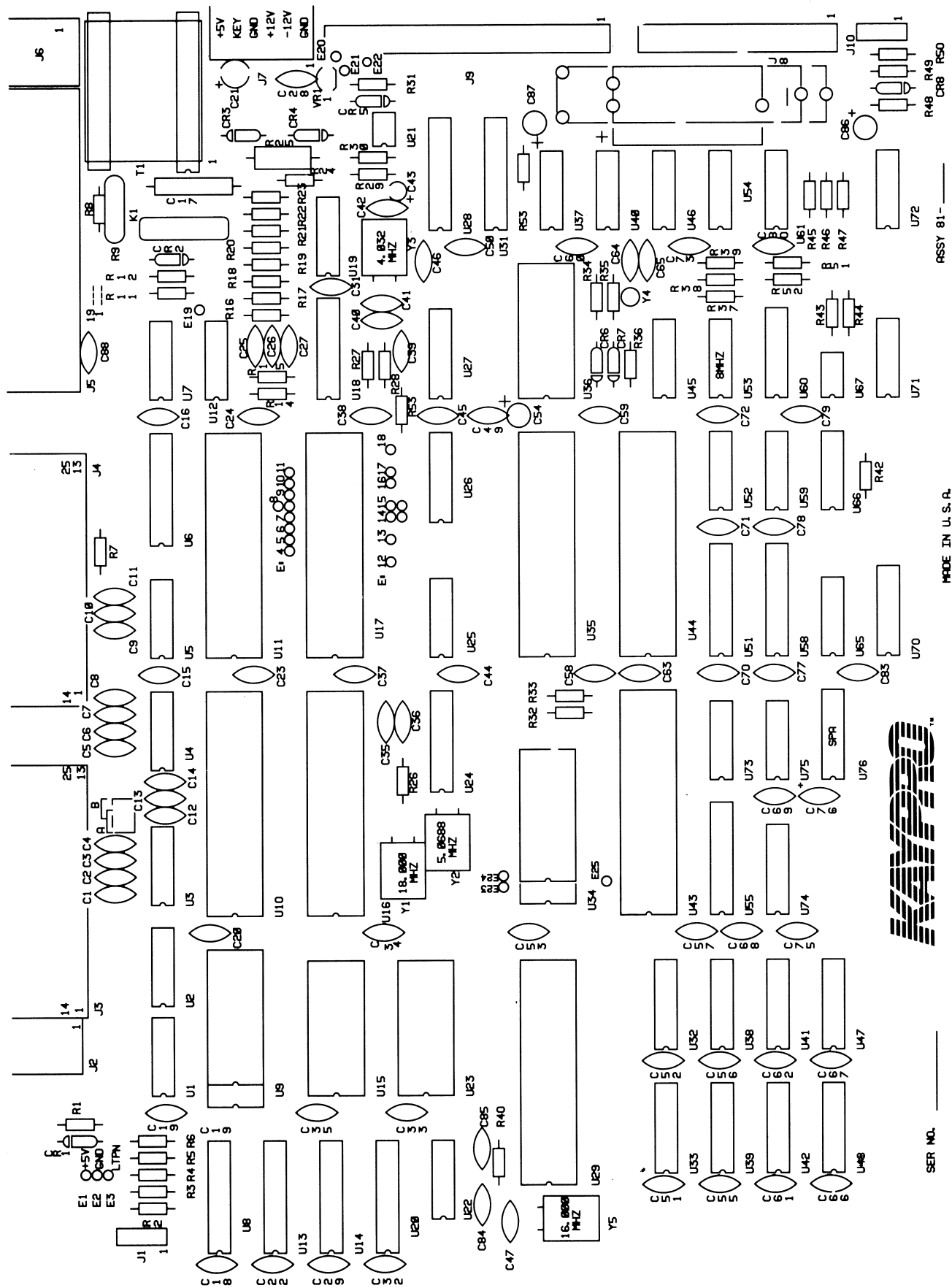
PROPRIETARY INFORMATION
Not for distribution or transmission to any other person or organization or company without the permission of Kaypro Corporation

U NOS. USED: 7, 12, 18,
19, 21, 25, 35, 36,
37, 49, 95, 96,
66

QTY REQD	PART OR IDENTIFYING NO.	NOMENCLATURE OR DESCRIPTION	MFR CODE	CIRCUIT REF.	ITEM NO.
PARTS LIST					
NON-LINEAR SYSTEMS, INC. DEL MAR, CALIFORNIA					
SCHEMATIC: MODEM & RJC					
KAYPRO 4184					
SIZE CODE IDENT NO. C 03626					
81-185					
SCALE WEIGHT SHEET 2 F					

UNLESS OTHERWISE SPECIFIED	CONTR. NO.	RELEASE DATE	BOARD REV B	(51-194)
DIMENSIONS ARE IN INCHES	DRAWN	APPR	REJ	1-2-83
TOLERANCES ON	CHECK			
DECIMALS				
FRACTIONS				
ANGLES				
± 0.10				
± 0.20				
± 1/32				
MATERIAL				
NEXT FINAL ASSY.				
NEXT FINAL ASSY.				
APPLICATION				
QTY.				

6.6 KAYPRO ROBIE (81-296-n)



SER NO.

MADE IN U. S. A.

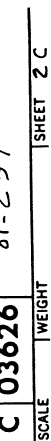
ASSY 81- _____

IC LIST, 81-296-n

Reference Designation		Description
U1, U54	7406	Hex inverter, open collector
U2, U25, U71	74LS14	Hex Schmitt inverter
U3, U5	1489	Quad Schmitt line driver
U4	1488	Quad line driver
U6, U13, U14, U55, U58	74LS373	Octal "D" latch
U7	74LS38	Positive-NAND buffer
U8	74LS374	Octal "D" flip-flop
U9	81-235	Character PROM (2732)
U10	81-189	Custom gate array
U11, U17	Z80A SIO	
U12	LM324	Op-Amp
U15, U23	6116	Video RAM
U16	6545EA	Video controller
U18	TMS 99532	Modem
U19	TMS 99531	Dialer
U20, U31	74LS245	Octal bus transceiver
U21	4N35	Photo Isolator
U22	3.9K	Pullup resistor, 14-pin DIP
U24	WD1943/ 8116	Dual programmable baud rate generator
U26, U27	74LS138	3/8 MUX
U28, U51	74LS244	Octal buffer/line driver
U29	81-194	Custom gate array
U32, U33, U38, U39, U41, U42, U47, U48	2164	64K x 1 RAM
U34	81-326	Boot PROM (2764)
U35	Z80A PIO	
U36	MM581678	Clock
U37, U45, U59, U72	74LS02	Quad NOR gate
U40	74LS00	Quad NAND gate
U43	Z80A CPU	
U44	1793	Floppy disk controller
U46	74LS04	Hex inverter
U52	74LS32	Quad OR gate
U70, U74, U75		Spares
U60	74LS195	4-bit shift register
U61	74LS08	Quad AND gate
U65	74LS10	Tri NAND gate
U66, U75	74LS74	Dual "D" flip-flop
U67	FDC9216	Data separator
U73	74HC00	Quad NAND gate



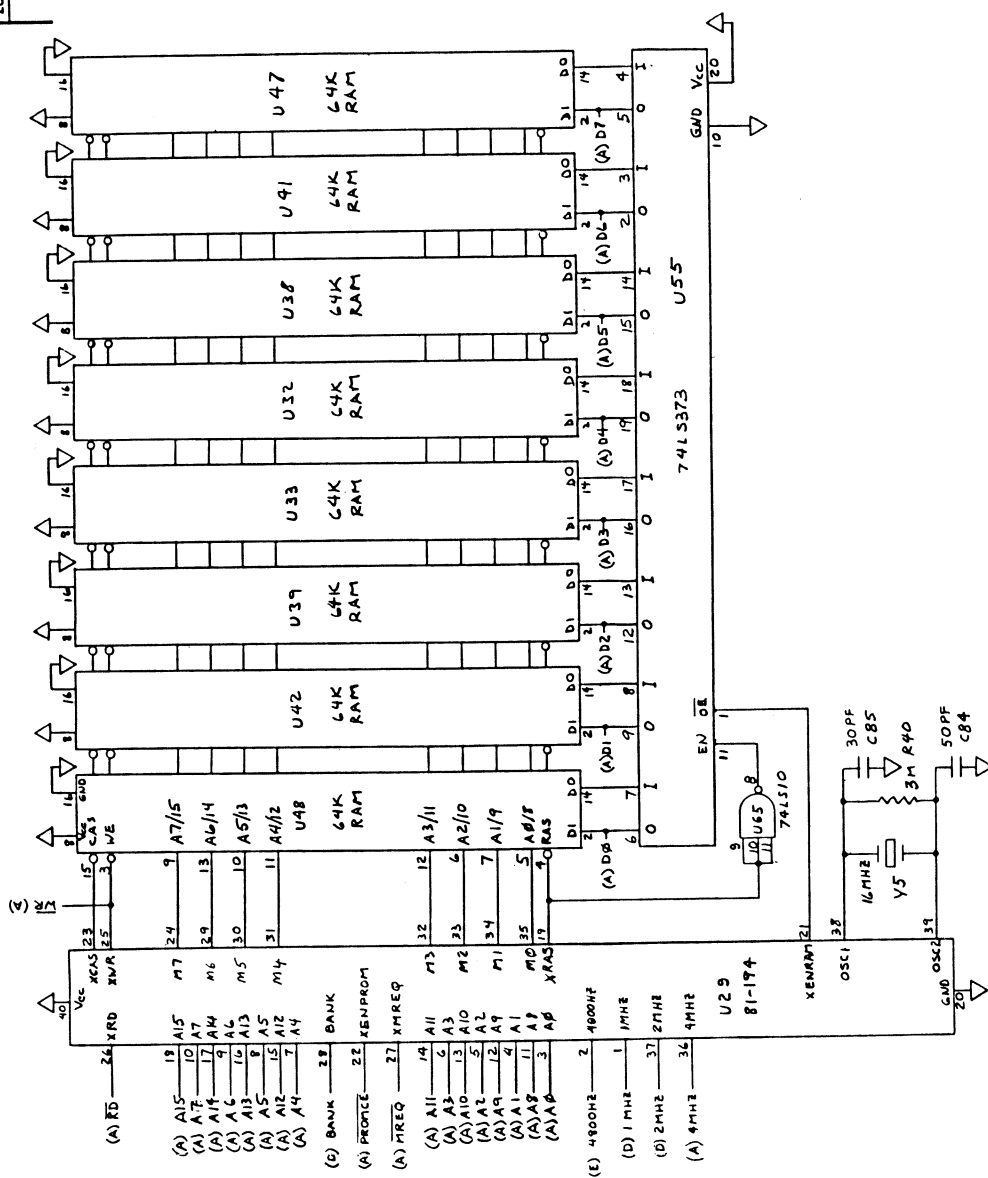
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Kardor Corporation



APPROXIMATE QUANTITY		APPROXIMATE QUANTITY	
NEXT ASSY.	FINAL ASSY.	NEXT ASSY.	FINAL ASSY.

REVISIONS		
ZONE	LT. EFFECT.	DATE

U NOS USED: 29, 48, 42,
39, 33, 32, 38, 41, 47, 55,
65



QTY	RECD	PART OR IDENTIFYING NO.	NOMENCLATURE OR DESCRIPTION	MFR. CODE	CIRCUIT REF.	ITEM NO.

PARTS LIST		

UNLESS OTHERWISE SPECIFIED		
DIMENSIONS ARE IN INCHES	CONTR. NO.	
TOLERANCES ON	DRAWN	
DECIMALS	CHECK	
FRACTIONS	APPR	
± 1/32	RELEASE DATE	

MATERIAL		
NEXT ASSY.	FINAL ASSY.	QTY.

APPLICATION		
NEXT ASSY.	FINAL ASSY.	QTY.

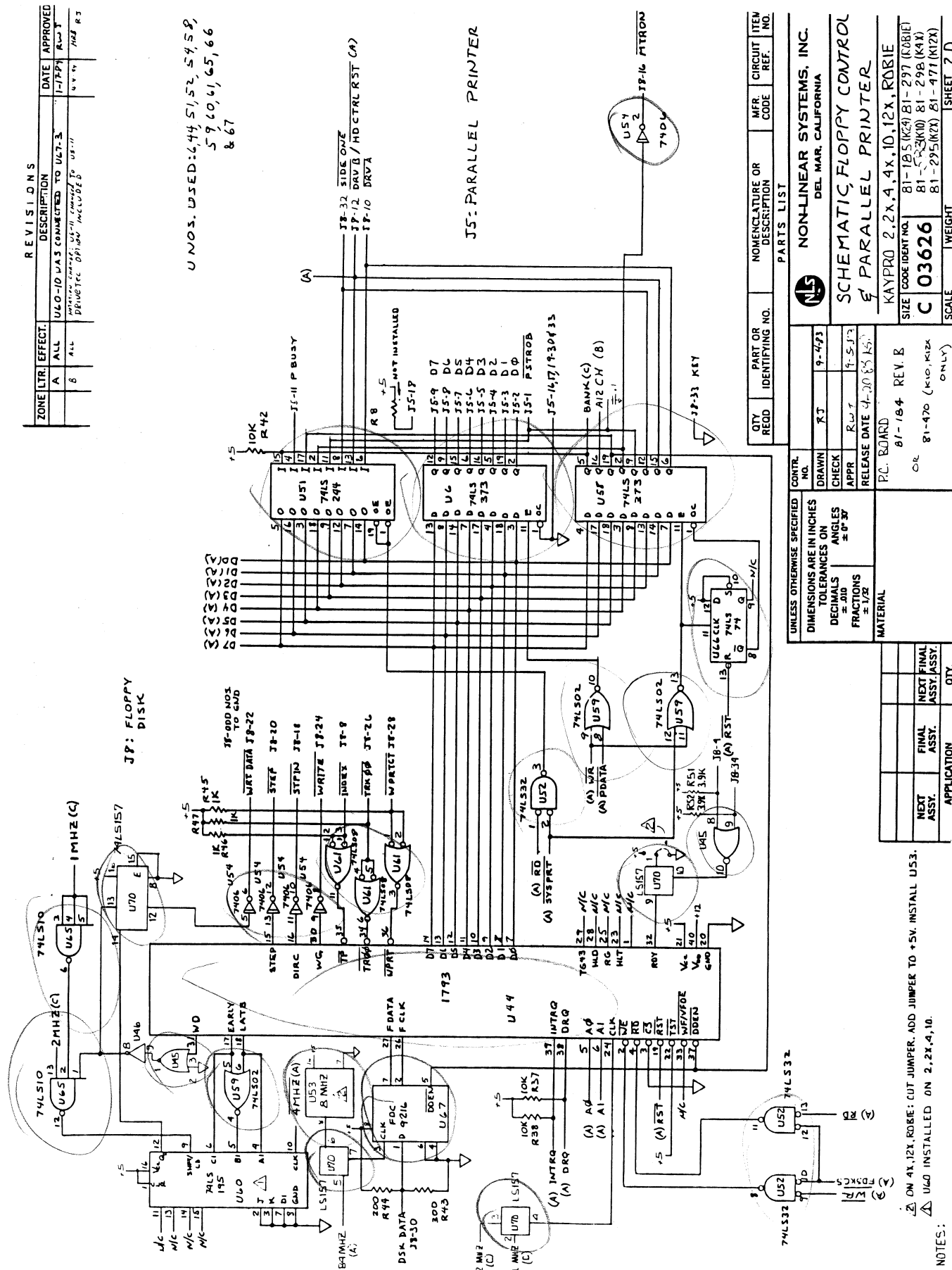
SCHEMATIC, RAM		
KAYPRO 2, 2x, 4, 4x, 10, 12x, ROSE		
SIZE	CODE IDENT NO.	
C 03626	81-185 (K20) 81-297 (ROSE)	
	81-53 (K10) 81-298 (K4X)	
	81-295 (K20) 81-471 (K12X)	
SCALE	WEIGHT	SHEET
		2 C

REVISIONS

ZONE	LTR	EFFECT	DESCRIPTION	DATE	APPROVED
A	ALL	U60-10 WAS CONNECTED TO U67-3	1-17-73		
B	ALL	MINOR CHANGE: US-11 CHANGED TO US-11	4-4-74		
		DRUGTEC OPTION INCLUDED	4-4-74		

U NOS. USED: 44, 51, 52, 54, 58,
59, 60, 61, 65, 66
867

JS: PARALLEL PRINTER



QTY	REQD	PART OR IDENTIFYING NO.	NOMENCLATURE OR DESCRIPTION	MFR. CODE	CIRCUIT REF.	ITEM NO.

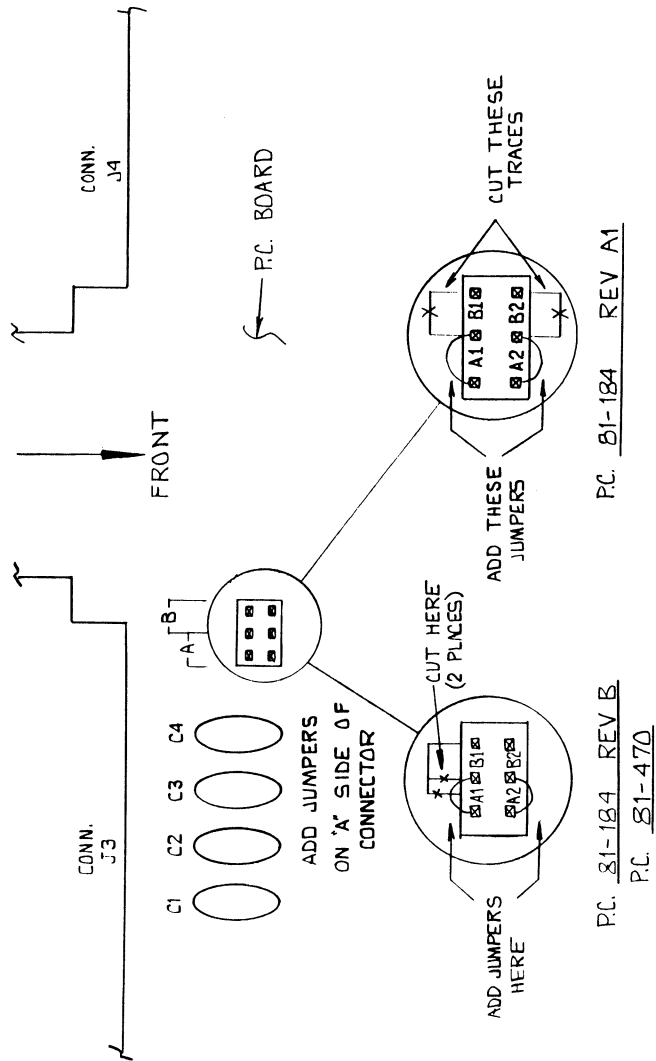
CONTR. NO.	DRAWN	CHECK	APPR.	RELEASE DATE	PC BOARD	REV. B	SCALE	WEIGHT	SHEET 2 D

UNLESS OTHERWISE SPECIFIED	CONTR. NO.	DRAWN	CHECK	APPR.	RELEASE DATE	PC BOARD	REV. B	SCALE	WEIGHT	SHEET 2 D

ON 4X, 12X, ROBE: CUT JUMPER, ADD JUMPER TO +5V, INSTALL U53.
U60 INSTALLED ON 2, 2X, 4, 10.

NOTES:

REVISIONS			
ZONE	LTR	EFFECT	DATE



P.C. B1-184 REV A1

P.C. B1-184 REV B
P.C. B1-470

QTY REQD	PART OR IDENTIFYING NO.	NOMENCLATURE OR DESCRIPTION	MFR. CODE	CIRCUIT REF.	ITEM NO.																																																												
<table border="1"> <tr> <th colspan="6">PARTS LIST</th> </tr> <tr> <td colspan="2">UNLESS OTHERWISE SPECIFIED</td> <td>CONTR. NO.</td> <td colspan="3">NLS NON-LINEAR SYSTEMS, INC. DEL MAR, CALIFORNIA</td> </tr> <tr> <td>DIMENSIONS ARE IN INCHES</td> <td>DRAWN</td> <td>K. BILINSKI</td> <td>5-23-84</td> <td colspan="2"></td> </tr> <tr> <td>TOLERANCES ON DECIMALS</td> <td>CHECK</td> <td>T. SOULE</td> <td>5-23-84</td> <td colspan="2"></td> </tr> <tr> <td>ANGLES</td> <td>APPR</td> <td>R. L. J.</td> <td>5-30-84</td> <td colspan="2"></td> </tr> <tr> <td>FRACTIONS</td> <td>RELEASE DATE</td> <td colspan="4">5-30-84</td> </tr> <tr> <td colspan="6">MATERIAL</td> </tr> <tr> <td colspan="2">NEXT ASSY.</td> <td colspan="2">NEXT ASSY.</td> <td colspan="2">NEXT ASSY.</td> </tr> <tr> <td colspan="2">APPLICATION</td> <td colspan="2">APPLICATION</td> <td colspan="2">APPLICATION</td> </tr> <tr> <td colspan="2">QTY.</td> <td colspan="2">QTY.</td> <td colspan="2">QTY.</td> </tr> </table>						PARTS LIST						UNLESS OTHERWISE SPECIFIED		CONTR. NO.	NLS NON-LINEAR SYSTEMS, INC. DEL MAR, CALIFORNIA			DIMENSIONS ARE IN INCHES	DRAWN	K. BILINSKI	5-23-84			TOLERANCES ON DECIMALS	CHECK	T. SOULE	5-23-84			ANGLES	APPR	R. L. J.	5-30-84			FRACTIONS	RELEASE DATE	5-30-84				MATERIAL						NEXT ASSY.		NEXT ASSY.		NEXT ASSY.		APPLICATION		APPLICATION		APPLICATION		QTY.		QTY.		QTY.	
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<table border="1"> <tr> <td>SIZE</td> <td>CODE IDENT NO.</td> <td>ASSY:</td> <td>81-185(K24)</td> <td>81-277(R01E)</td> <td>81-298(K4)</td> </tr> <tr> <td>B</td> <td>03626</td> <td></td> <td>81-534(K0)</td> <td>81-298(K4)</td> <td>81-471(K12X)</td> </tr> <tr> <td colspan="3">SCALE</td> <td colspan="3">SHEET 2 G</td> </tr> </table>						SIZE	CODE IDENT NO.	ASSY:	81-185(K24)	81-277(R01E)	81-298(K4)	B	03626		81-534(K0)	81-298(K4)	81-471(K12X)	SCALE			SHEET 2 G																																												
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SCALE			SHEET 2 G																																																														

6.7 MAINBOARD REMOVAL

1. Turn off the machine and disconnect the AC power (5.1).
2. Remove the chassis hood (5.1).
3. Remove the two screws from the front of the mainboard that attach the mainboard and the two plastic standoffs.
4. Remove the screws from the rear, top of the chassis that secure the mainboard. The KAYPRO 2/83 and 4/83 each have four Phillips-head screws and two hex-head screws. The KAYPRO 10, 2/84 and 4/84 each have three Phillips-head screws and four hex-head screws.
5. Remove the power, reset, and video plugs. On the KAYPRO 2/83 and 4/83 these are J1, J5, and J7. On the KAYPRO 10, 2/84 and 4/84 these are J1, J7, and J10.
6. Remove the ribbon cable(s). On the KAYPRO 2/83 and 4/83 this is J6. On the KAYPRO 10 they are J8 and J9. On the KAYPRO 2/84 and 4/84 this is J8.
7. Remove the mainboard.

MAINBOARD INSTALLATION

1. Set the mainboard on the plastic standoffs, insert the screws, but do not tighten them yet.
2. Align the ports and the keyboard jack with the openings on the rear, top of the chassis.
3. Insert the screws through rear of chassis into mounting holes. Do not tighten yet.
4. The KAYPRO 2/83 and 4/83 each have four Phillips-head and two hex-head screws. The KAYPRO 10, 2/84 and 4/84 each have three Phillips-head and four hex-head screws.
5. Tighten the screws on the rear of the chassis and the screws that go into the plastic standoffs.
6. Replace the power, reset, and video plugs.
7. Replace the ribbon cable(s).

7.0 CRT ASSEMBLIES

7.1 HARDWARE DESCRIPTIONS AND ADJUSTMENTS

Figure 7.1
Dotronix video board

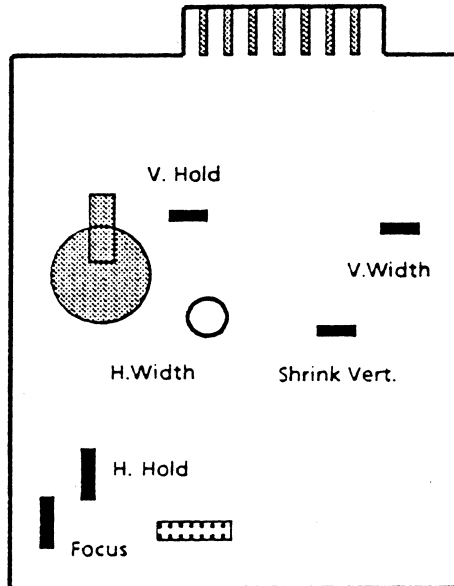
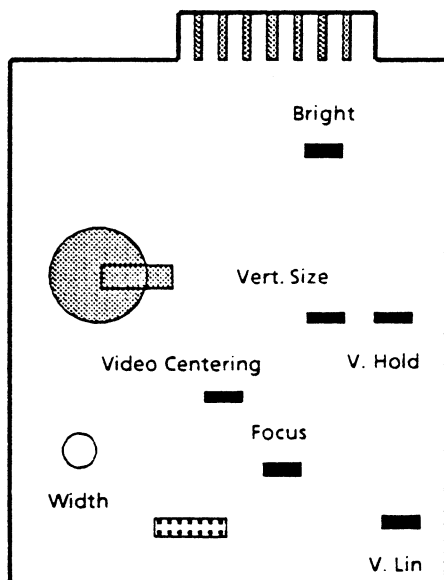


Figure 7.2
Elston video board



VIDEO ADJUSTMENTS

At the current time Kaypro is using CRT assemblies produced by Dotronix, Elston, Micrex and Toshiba. The adjustment pots are positioned in different locations on the boards and can be located by using figures 7.1 through 7.4. These pots are factory aligned and normally need no adjustment. The purpose of these adjustments is to obtain the correct size, centering, and brightness of the display.

Use only non-metallic tools when making these adjustments.

Before any adjustments are made on the video board, the ALIGN diagnostic should be invoked. This will fill the screen with H's and aid in adjustment.

HORIZONTAL CENTERING

Check to see that the pattern is centered on the screen. Adjust the horizontal hold pot until the display is correctly centered. On the Elston video board, adjust the video centering pot.

VERTICAL SIZE AND LINEARITY

The following two procedures are to be performed alternately until correct display is obtained.

Adjust the vertical size pot to obtain pattern height of approximately 4-7/8 inches.

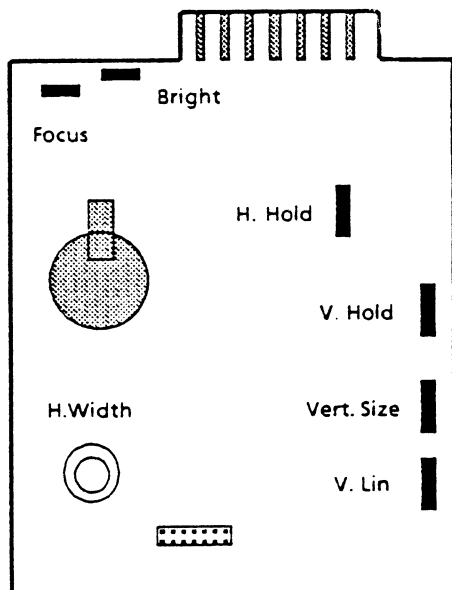
Adjust vertical linearity pot until all characters are the same vertical size, top to bottom.

HORIZONTAL WIDTH

Adjust the horizontal width pot to obtain a display width of approximately 7 inches.

HORIZONTAL HOLD (KAYPRO 2/83 and 4/83 with Dotronix only)

Figure 7.3
Toshiba video board



These procedures should be performed to adjust horizontal hold.

Attach one end of a test jumper to TP2 (test point 2). Attach the other end of the jumper to the heat sink at Q3.

Adjust R43 (horizontal hold pot) until the display either stops scrolling or almost stops. (sometimes they don't stop scrolling completely)

Disconnect the test jumper.

Adjust the horizontal deflection rings as needed.

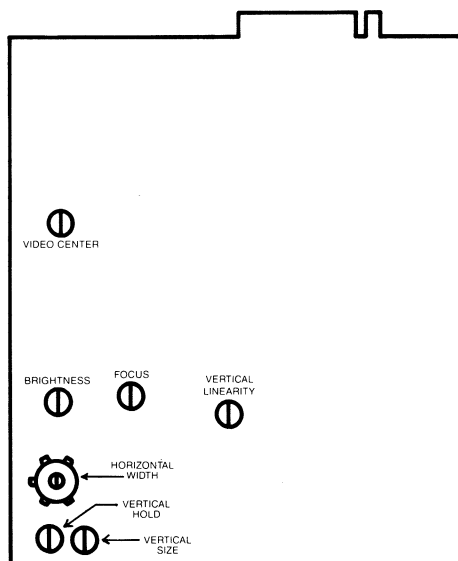
FOCUS

Adjust the focus pot until the edges and center of the display image are in focus.

BRIGHTNESS

To prolong the life of the CRT, the maximum brightness obtainable should not be excessive. If the brightness is excessive, adjust the brightness pot. If the raster lines are visible, the brightness pot should be adjusted until they disappear.

Figure 7.4
Micrex video board

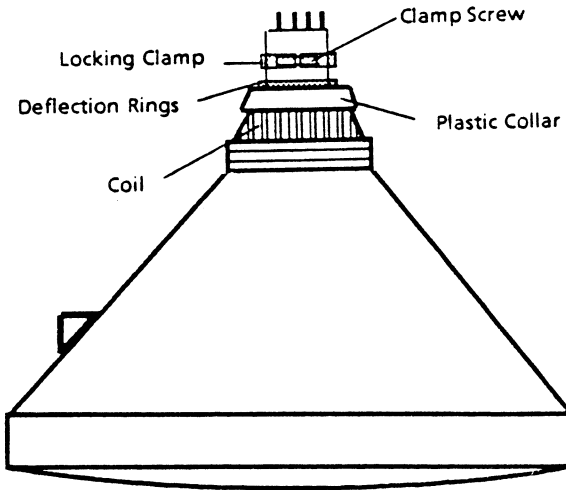


ALIGNING CRT YOKE

*** CAUTION ***

HIGH VOLTAGE IS PRESENT AT THIS POINT

Figure 7.5
Yoke alignment



Loosen the screw on the locking clamp (figure 7.5).

Grasp the white collar on the rear of the coil.

Turn the collar in the direction required to square the display.

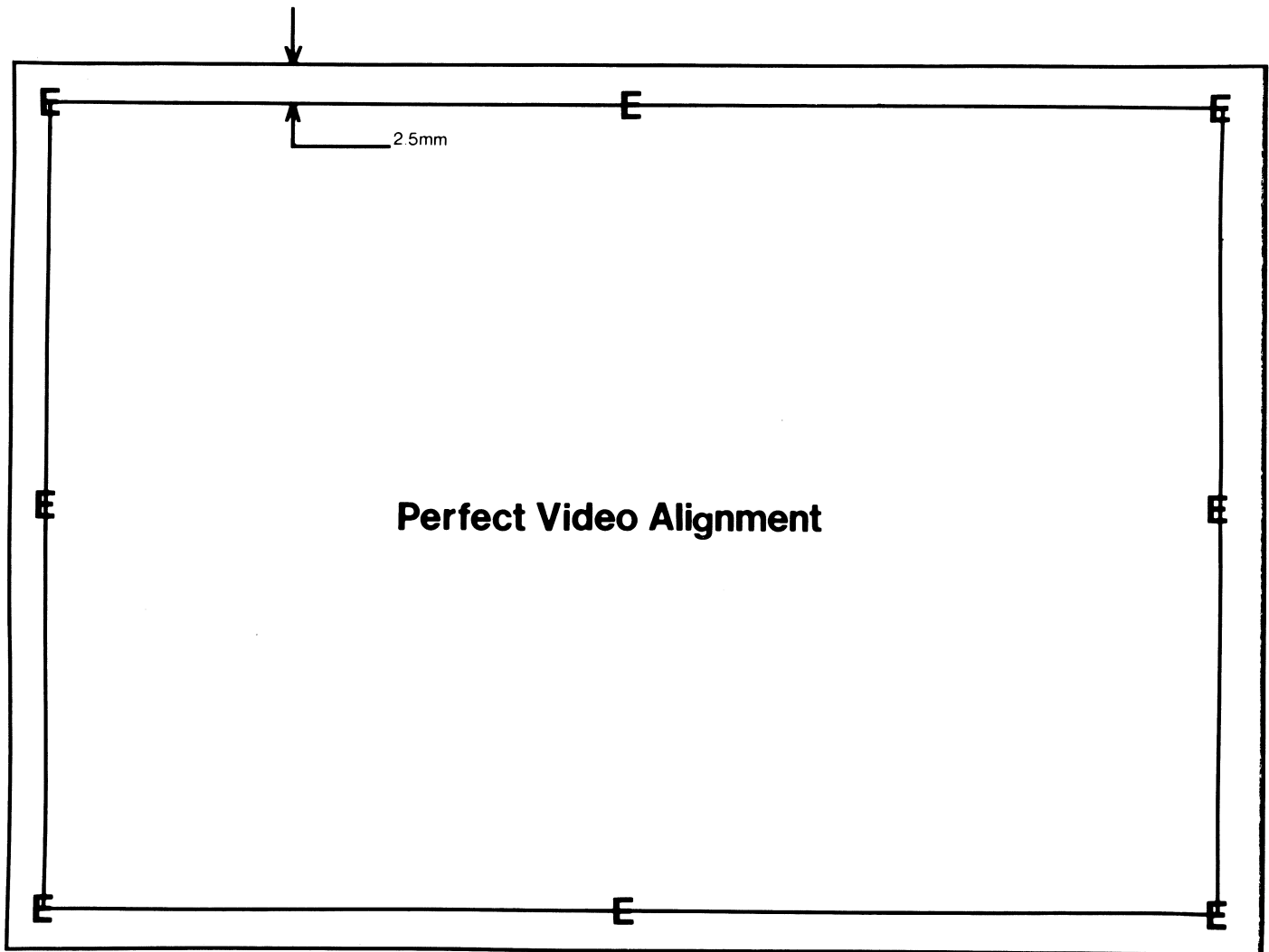
Turn the collar slightly past the alignment point, as it will turn back slightly when released.

Tighten the locking screw.

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7.2 PERFECT VIDEO ALIGNMENT EXAMPLE

Below is an example of perfect video alignment with a capital E positioned along the borders. You can copy this diagram onto a piece of clear, stiff plastic to use as a template over the screen.



7.3 VIDEO SIGNALS, ON KAYPRO MAINBOARD

Below is a list of the video signals on connector J1 of the mainboard and the voltages on the video board. Repeated removal and installation of the video board connector will loosen the connection and produce poor contact. Malfunctions due to a loose connector can be very difficult to trace.

Video comes from the main board as separate signals, not as a composite like a TV signal.

A simple check for 12V on the video board is to turn the BRIGHTNESS pot up, then turn the rear panel brightness control down. If 12V is present on the board, you should be able to see the raster lines on the screen.

Video signals on main board at J1

- Pin 1 - Horizontal Sync.
- Pin 2 - Key
- Pin 3 - Video
- Pin 4 - Vertical Sync.

Voltages on Video Board

- Pin 1 → 0v
- Pin 6 → 0v
- Pin 7 → 12v
- Pin 8 → 0v
- Pin 9 → 4v approx. (this voltage controls brightness)
- Pin 10 → 0v

7.4 CRT ASSEMBLY REMOVAL, (EXCEPT ROBE)

1. Turn off the machine and disconnect the AC power (5.1).
2. Remove the chassis hood (5.1).
3. Remove the mainboard (6.7).
4. Remove the four screws that connect the video PCB and the bottom of the chassis. This should be done from the outside, bottom of the chassis, not from inside the machine.
5. Remove the video connector from the back of the video PCB.
6. Remove the four screws that connect the CRT to the front of the machine chassis.
7. Remove the CRT assembly from the chassis.
8. Remove the four plastic standoffs from the CRT PCB. These can be used on the new CRT PCB.

CRT ASSEMBLY INSTALLATION

*** Note: Before starting with CRT installation, check to see if the small plastic standoffs are attached to the bottom of the CRT PCB. If they are not attached, use the ones from the old board.

1. Lower the CRT assembly into the chassis.
2. Position the CRT so that the top mounting holes are aligned with the two metal standoffs on the chassis.
3. Insert screws through the top two mounting holes on the CRT and into the standoffs, but do not tighten.
4. Insert screws through the bottom two mounting holes on the CRT and into the standoffs. Tighten these two screws and the top two screws.
5. Hold the CRT PCB in place and tilt the machine up so that it is sitting on the cord wraps.
6. Align the plastic standoffs on the PCB with the holes in the bottom of the chassis.
7. Insert the four screws and tighten securely.
8. Replace the video connector on the back of the video PCB.

8.0 POWER SUPPLIES

8.1 INTRODUCTION

Kaypro is using three different brands of power supplies at the present time. These are Astec, Boschert, and Cal D.C. These three power supplies are interchangeable with any of the Kaypro computers, if the power supply being changed is a new one from the factory.

***** EXCEPTION *****

If a power supply is removed from a 2/83 or a 4/83, and it's going to be used in another computer, it **MUST** be used in a 2/83 or 4/83. The power supplies used in 2/83 and 4/83 computers are not interchangeable with other Kaypro computers.

There are no authorized dealer repairs that can be made on any of the power supplies. The **ONLY** authorized dealer service to power supplies is 220V configuration. Each brand of power supply can be configured for 220V operation.

8.2 DESCRIPTION AND 220V CONFIGURATION INSTRUCTIONS

Two fuses are associated with each of the power supplies. One fuse (2 amp) is mounted on the rear of the chassis and accessible from outside the computer. The other fuse (2.5 amp) is mounted directly on the power supply board.

Disconnect AC power from the computer whenever replacing fuses. Be especially careful when replacing the fuse on the power supply board. Use of a fuse replacement tool is recommended due to the difficulty of reaching this component.

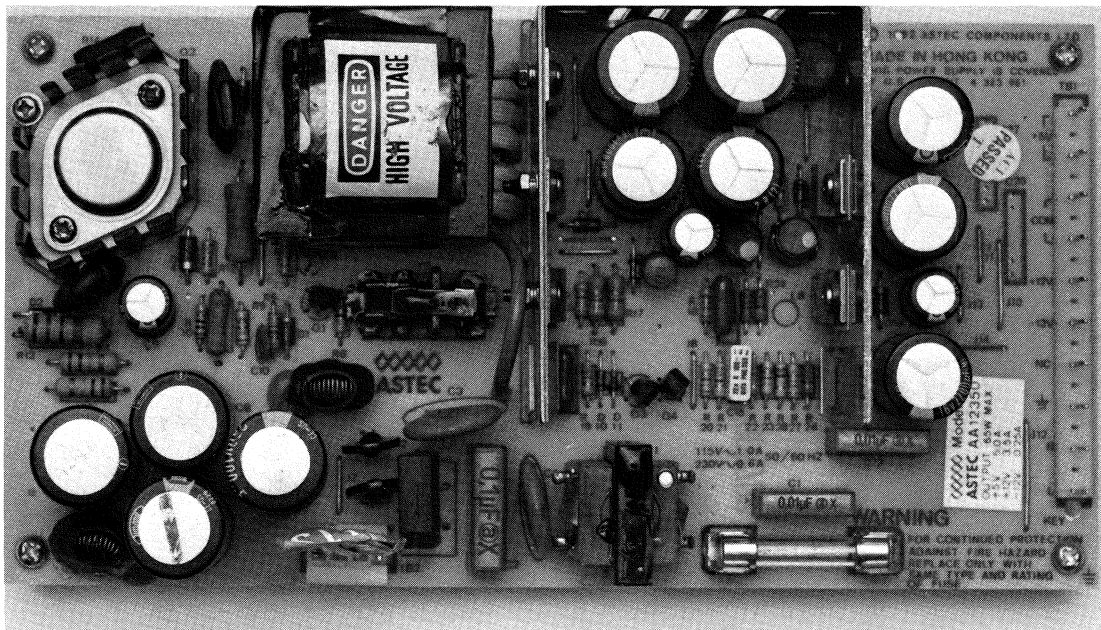
220V CONFIGURATION

Astec Power Supply

Identified by a beige board with a white label on the right of the component side. The label will have the name ASTEC, followed by a model number, and the output voltages.

Locate the white shorting block labelled TB2. If the board is configured for 110V, a pin will be visible on the right of the shorting block.

Remove the shorting block and shift it to the right so that it covers the pin. A different pin should now be exposed on the left of the shorting block. The power supply is now configured for 220V use.



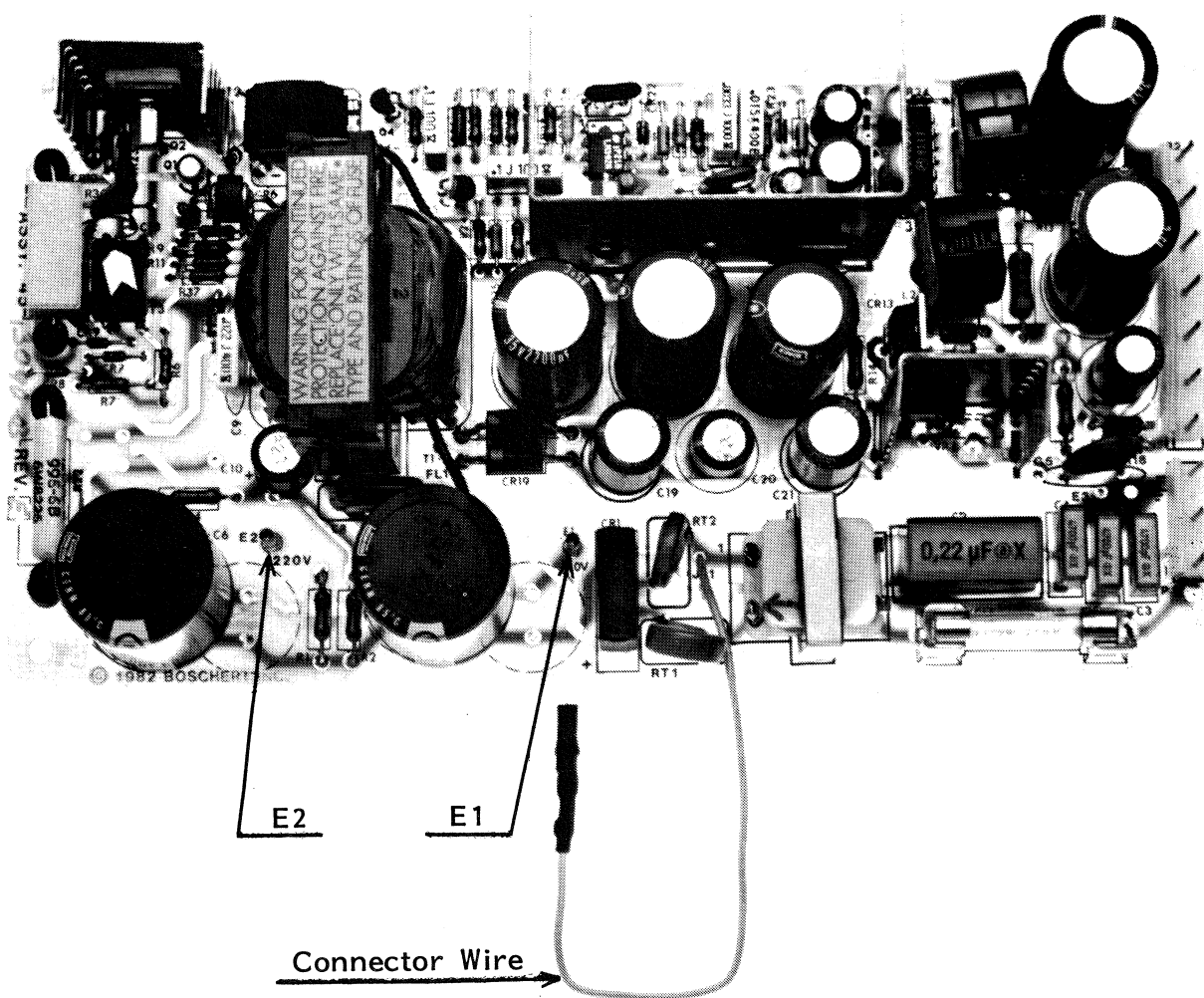
220V CONFIGURATION

Boschert Power Supply

Identified by the word "Boschert" on the component side of the board at lower right and above the fuse and capacitor C2.

Locate a six-inch wire that is soldered to point JP1. If the board is configured for 110V, the other end of the wire is attached to point E1 (labelled 110V).

Unplug the wire from point E1 and plug it into point E2 (labelled 220V). Point E2 is to the left of point E1. The power supply is now configured for 220V use.



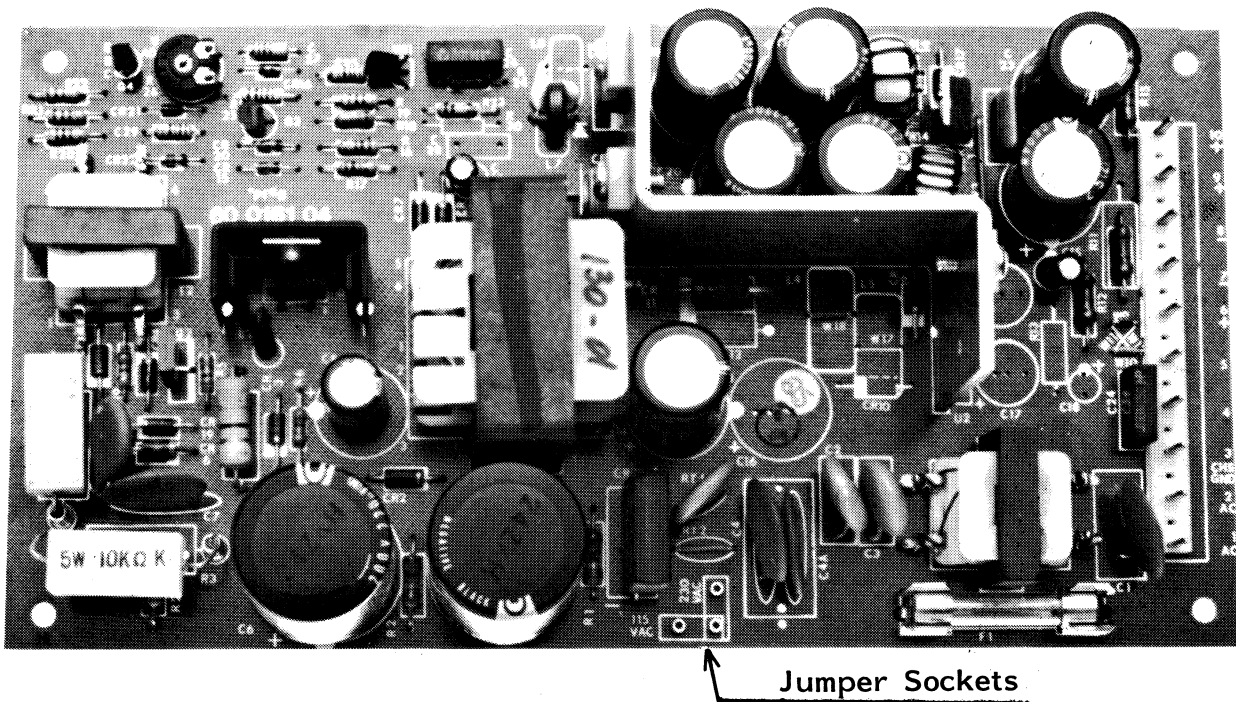
220V CONFIGURATION

Cal D.C. Power Supply

Identified by a bright blue circuit board. A white sticker with "California DC", model, and serial number is located on solder side of board.

Locate a jumper block labelled 115VAC and 230VAC. With the jumper in the 115VAC position the board is configured for 110V.

Remove the jumper from the socket labelled 115VAC and position it in the socket labelled 230VAC. The power supply is now configured for 220V use.



8.3 POWER SUPPLY REMOVAL

1. Turn off the machine and disconnect the AC power (5.1).
2. Remove the chassis hood (5.1).
3. Remove the mainboard (6.7).
4. Remove the four screws that connect the power supply and the back of the chassis. These are easier to remove from outside the back of the chassis than from inside the machine.
5. Remove the power plug from the power supply.
6. Remove the tie wraps from the plastic standoffs.
7. Remove the power supply.

POWER SUPPLY INSTALLATION

1. Position the power supply inside the chassis so that the four plastic standoffs are aligned with the four mounting holes in the chassis. Be certain that the power connector is on the side nearest the drives.
2. Place the tie wraps on the plastic standoffs.
3. Insert four screws through the rear of the chassis and into the plastic standoffs of the power supply.
4. Tighten the screws securely.
5. Replace the power connector.
6. Replace the mainboard (6.7).
7. Replace the chassis hood (5.1).

9.0 DISKETTE DRIVES

9.1 INTRODUCTION

All diskette drive types (except the high-density drives in the ROBIE and 4X) are functionally interchangeable between manufacturers.

Because of the number of manufacturers from which Kaypro gets its diskette drives, no attempt is made in this manual to show alignment procedures for diskette drives. To perform drive alignments you will need a drive manual for the particular model, a Dysan Alignment Diskette (Dysan's part number: 224/2A), the knowledge and an oscilloscope. Drive manuals are available through the Hardware Technical Support Department for a nominal fee.

However, since the company recognizes the fact that many customers want a KAYPRO computer in which both drives have the same outward appearance, we provide a guide for determining models of diskette drives from the placement of the LED and the drive door closure.

This section also contains diagrams and instructions on jumpering the various models of diskette drives to be used as either A or B (or, in the case of the KAYPRO 10, C) drive.

DISK DRIVE CLEANING

Generally speaking the majority of people clean disk drives too often. Unless the environment is especially dusty or dirty, under commercial use there is no reason to clean the drives more than twice a year.

Use care in selecting a cleaning kit. Many of the drive head cleaning kits on the market are very abrasive. Cleaning is done by the liquid solution, not by mechanical scrubbing.

*****NOTE***** The manufacturer (Drivetec) of the high-density drives in the ROBIE, and 4X recommends only four brands of head-cleaning kits for their drives. The letter from Drivetec states:

"THE FOLLOWING CLEANING DISKS HAVE BEEN EVALUATED
AND ARE RECOMMENDED FOR USAGE ON THE DRIVETEC DISK
DRIVE WHEN HEAD CLEANING IS DEEMED NECESSARY:

- 1) SCOTCH #7440
- 2) HEAD COMPUTER PRODUCTS 5 1/4 CLEANING DISK
- 3) PERFECT DATA 5 1/4 CLEANING DISK
- 4) FLOPPICLENE 5 1/4 CLEANING DISK"

Refer to the symptom-fix guide for troubleshooting hints relative to the floppy drives, the Winchester hard disk, and the Drivetec (high-density) drives.

9.2 WHICH BRAND OF DRIVE IS IT?

The following figures represent the face plates of the different brands of half-height floppy drives that are used in Kaypro computers. The drive door closure and the LED position can be used to reference the drive.

Fig. 9.1, TANDON Half-Height

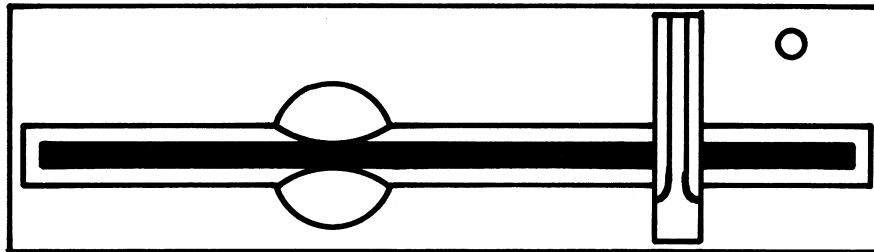


Fig. 9.2, EPSON

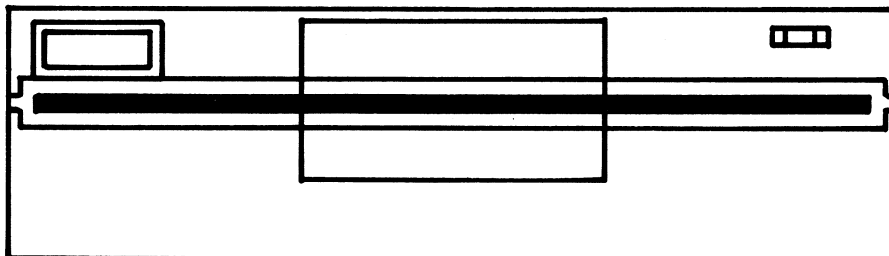


Fig. 9.3, SHUGART

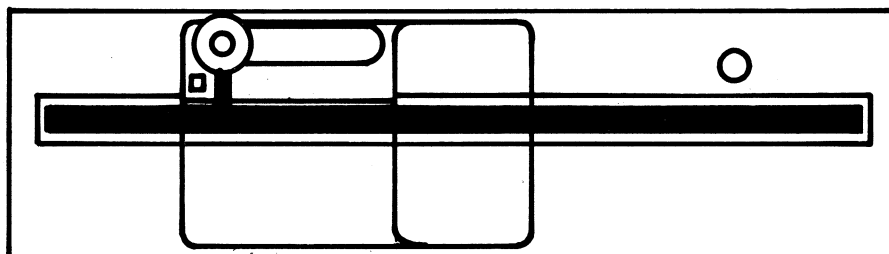


Fig. 9.4, TOKYO ELECTRIC

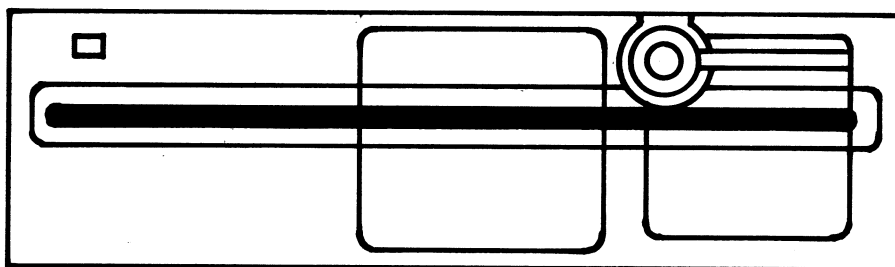


Fig. 9.5, TOSHIBA

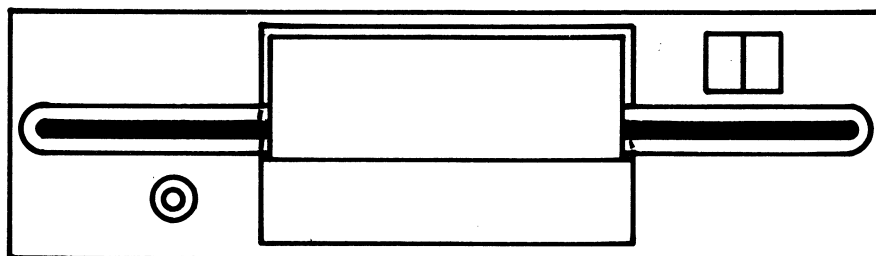


Fig. 9.6, HI-TECH

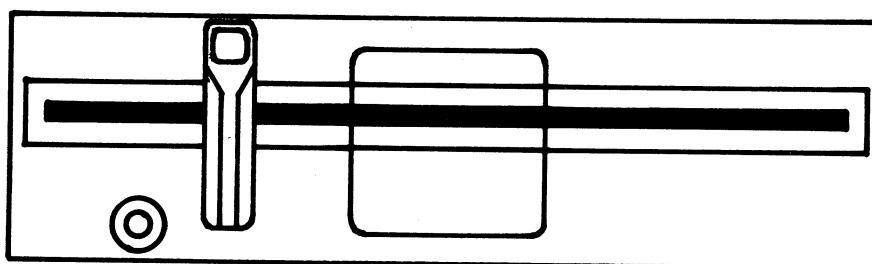


Fig. 9.7, DRIVETEC

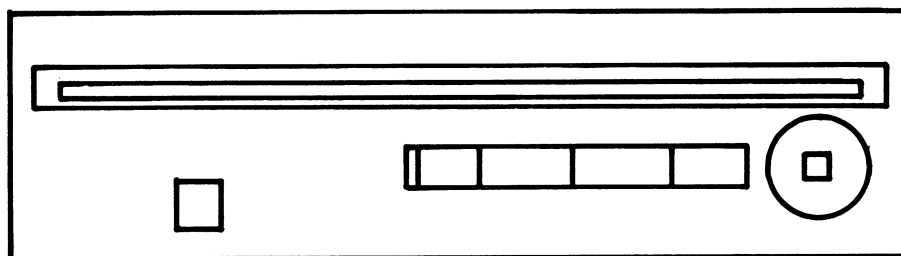
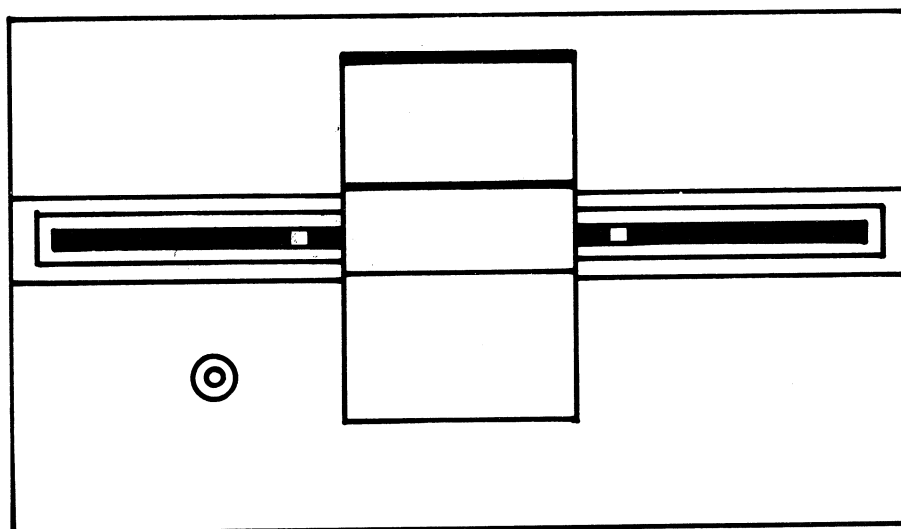
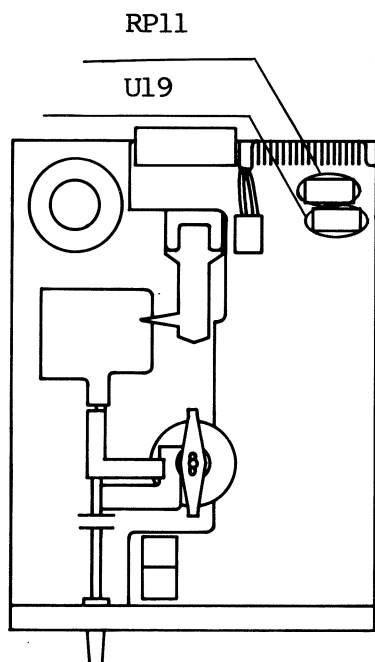


Fig. 9.8, TANDON Full-Height



9.3 JUMPERING DIAGRAMS

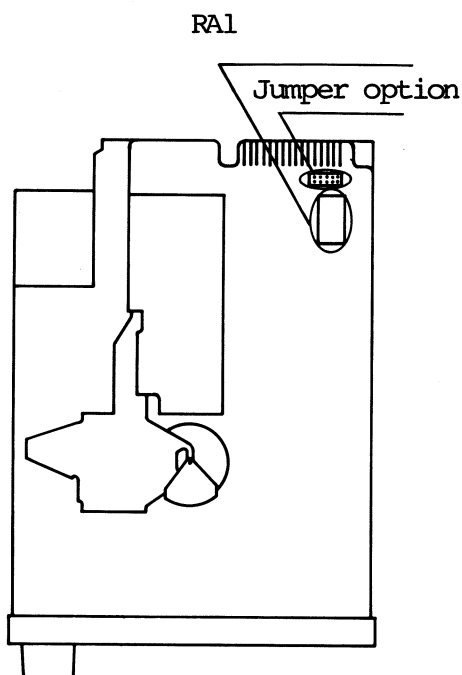
Figure 9.8
Tandon diskette drive



TANDON

Used only on KAYPRO 10, Pins 2 and 15 on U19 are jumpered and a 470-ohm terminating resistor pack is inserted in RP11.

Figure 9.9
Epson diskette drive



EPSON

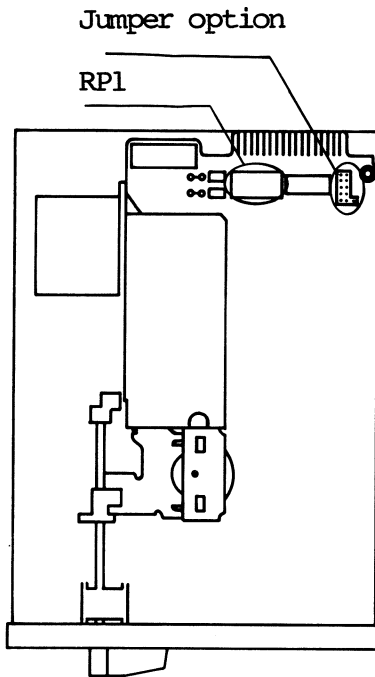
Next to the connector for the data cable is a black plug with ten pins. These pins are jumpered to change the configuration of the drive. They are MX, 0, 1, 2, 3.

KAYPRO 10: The two pins in the "0" position should be jumpered and a 470-ohm terminating resistor inserted in RA1.

A DRIVE: Jumper the two pins in the "0" position. No terminating resistor is needed.

B DRIVE: Jumper the two pins in the "1" position. Insert a 470-ohm terminating resistor into RA1.

Figure 9.10
Shugart diskette drive



SHUGART

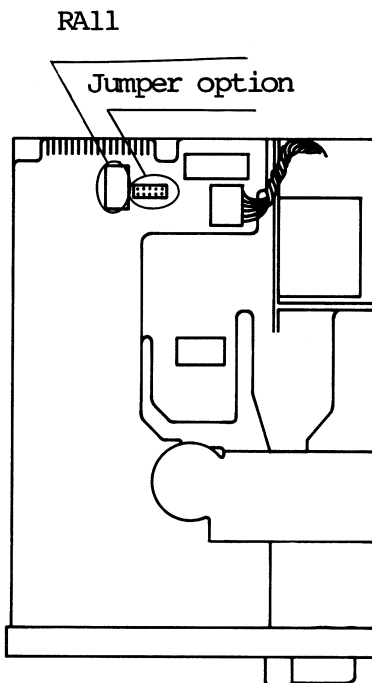
Next to the connector for the data cable is a blue plug with eleven pins. These pins are 1, 2, 3, 4, MX.

KAYPRO 10: Jumper the two pins in the "1" position. In the "MX" position, there are three pins. Jumper the one in the center and the one on the left of it. Insert a 470-ohm terminating resistor into RP1.

A DRIVE: Jumper the two pins in the "1" position. In the "MX" position, jumper the pin in the center and the pin on the left of it. No terminating resistor is necessary.

B DRIVE: Jumper the two pins in the "2" position. In the "MX" position, jumper the pin in the center and the pin on the left of it. Insert a 470-ohm terminating resistor into RP1.

Figure 9.11
Tokyo Electric
diskette drive



TOKYO ELECTRIC

Next to the connector for the data cable is a blue plug with ten pins. These pins are DS0, DS1, DS2, DS3, MX.

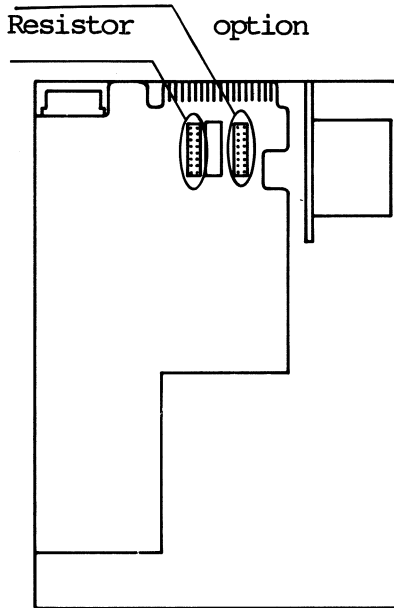
KAYPRO 10: Jumper the two pins in the "DS0" position. Insert a 470-ohm terminating resistor into RA11.

A DRIVE: Jumper the two pins in the "DS0" position. No terminating resistor is necessary.

B DRIVE: Jumper the two pins in the "DS1" position. Insert a 470-ohm terminating resistor into RA11.

Figure 9.12
Toshiba diskette drive

Jumper option



TOSHIBA

Next to the connector for the data cable is a black plug with sixteen pins. These are 1, 2, 3, 4, LI, LD, HD, HM. Next to the black plug, is a 470-ohm terminating resistor with the letters "DM" to the left of it. Locate this resistor. On the other side of the resistor is another black plug. This plug has the letters "RM" to the left of it and the letters "PJ5" to the right of it. It has sixteen pins. For the terminating resistor to function, all pins must be jumpered EXCEPT the two next to the letters "RM". When the resistor is not needed, none of the pins should be jumpered.

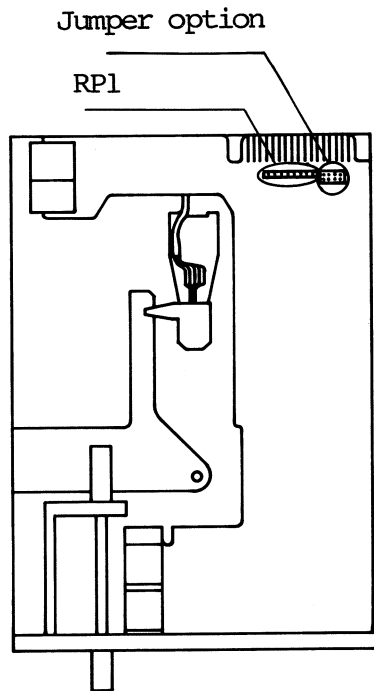
KAYPRO 10: Jumper the two pins in the "1" position, the two pins in the "LD" position, and the two pins in the "HM" position. The terminating resistor is needed.

A DRIVE: Jumper the two pins in the "1" position, the two pins in the "LD" position, and the two pins in the "HM" position. The terminating resistor is not needed.

B DRIVE: Jumper the two pins in the "2" position, the two pins in the "LD" position, and the two pins in the "HM" position. The terminating resistor is needed.

Note: Some Toshiba drives are manufactured using a circuit board that's different in appearance from the one in the above illustration. Although the board appears different, the circuitry is the same and the drive should be jumpered using the above instructions.

Figure 9.13
Hi-Tech diskette drive



HI-TECH

Next to the connector for the data cable is a black plug with eight pins. These pins are DS0, DS1, DS2, DS3.

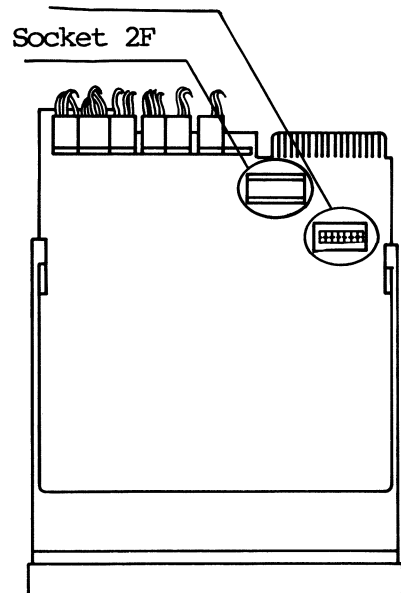
KAYPRO 10: Jumper the two pins in the "DS0" position. Insert a 470-ohm SIP terminating resistor into RP1.

A DRIVE: Jumper the two pins in the "DS0" position. No terminating resistor is necessary.

B DRIVE: Jumper the two pins in the "DS1" position. Insert a 470-ohm terminating resistor into RP1.

Figure 9.14
Tandon full-height
diskette drive

Option shunt 1E



TANDON

Used only in KAYPRO II AND KAYPRO 4.

A DRIVE: Pins 1-14 and 2-13 should be jumpered in option shunt block 1E.

B DRIVE: Pins 1-14 and 3-12 should be jumpered in option shunt block 1E. Insert a 470-ohm terminating resistor into socket 2F.

9.4 HIGH-DENSITY (DRIVETEC) DRIVES

The high-density diskette drives are currently being offered in the KAYPRO ROBIE, 4X and 12X. These are 5-1/4 inch, double-sided drives with 192 tracks per inch. Each drive has 160 cylinders with a total of 320 tracks and a formatted storage capacity of 2.6 megabytes.

HIGH-DENSITY DISKETTES

The diskettes used with the high-density drives MUST be pre-formatted 17 sector, 192 TPI diskettes.

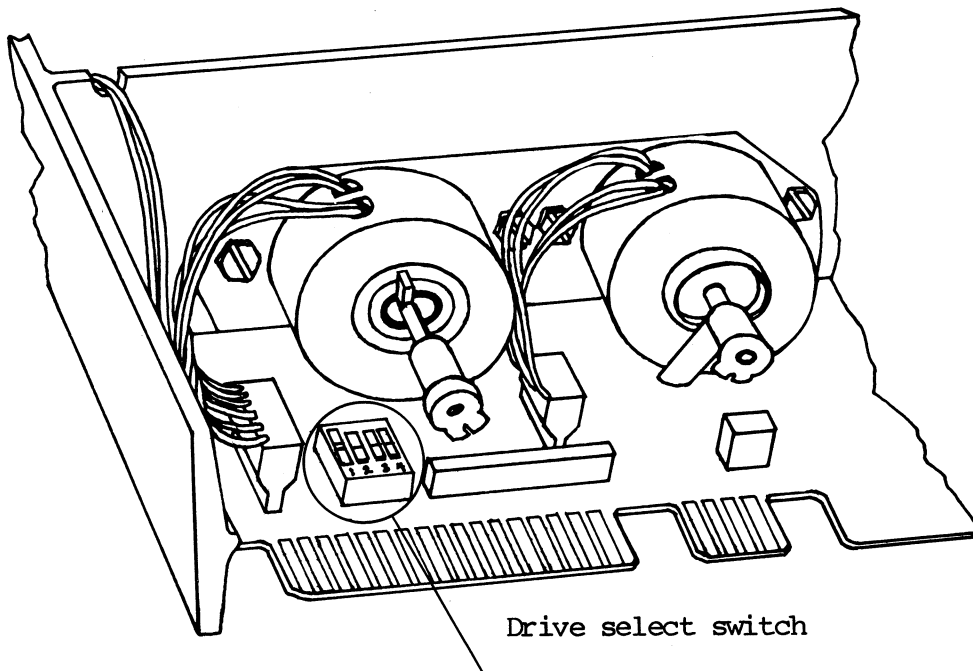
DRIVE CONFIGURATION

Next to the connector for the data cable is a drive select switch-- DS1 through DS4.

A DRIVE: DS1 should be in the "on" position. All other switches should be in the "off" position.

B DRIVE: DS2 should be in the "on" position. All other switches should be in the "off" position.

12X: DS1 should be in the "on" position. All other switches should be in the "off" position.



9.5 DISKETTE DRIVE REMOVAL

1. Turn off the machine and disconnect the AC power (5.1).
2. Remove the chassis hood (5.1).
3. Remove the ribbon cable, power plug, and ground wire from the rear of the drive.
4. Remove the four screws that secure the drive in the drive shield.
5. Slide the drive out of the drive shield through the front of the machine.

Note: If the drive is hard to slide out of the drive shield unit, loosen the screws holding the other drive in place.

DISKETTE DRIVE INSTALLATION

1. Configure the new drive. (A Drive or B Drive) Refer to pages 9—4 to 9—8 explaining drive configuration.
2. Slide the drive into the drive shield.
3. Align the mounting holes on the drive with the holes on the drive shield.
4. Insert four screws through the shield and into the drive mounting holes.
5. After all the screws are inserted, tighten them securely.
6. Replace the ribbon cable, power plug, and ground wire on the rear of the new drive.

10.0 HARD DISK DRIVES (KAYPRO 10 and 12X)

10.1 INTRODUCTION

Kaypro Corporation uses hard disk drives from a number of different manufacturers. As with the diskette drives, all models of hard disk drives are functionally interchangeable.

No adjustments are meant to be made by dealers (or are made by Kaypro technicians) on hard drives. And, since recovering information from a hard disk which has "crashed" requires a clean room, no directions for performing such an operation are included in this manual. Be aware that true head crashes are very rare occurrences. Most apparent hard disk problems are actually problems within other components and can be corrected without replacing the hard drive itself. Refer to the symptom-fix guide for help in troubleshooting apparent hard drive problems.

We cannot stress strongly enough that dealers instruct their customers to ALWAYS back up their data while working on a hard disk. In many cases NOTHING can be done about the loss of data in a hard-drive failure.

10.2 DESCRIPTION

The hard disk drives used in the KAYPRO 10 and the 12X are industry standard, 5-1/4 inch half-height drives offering Winchester technology. This technology includes sealed media and drive heads, with an air filtration system that prevents contamination. Since the drives are sealed, there are no dealer serviceable components on the drive. The only authorized dealer service to the hard disk drives is drive configuration.

10.3 HARD DRIVE CONFIGURATION

MICROSCIENCE

Figure 10.1
Microscience
9 Position Switch



There are two types of drive selection switch banks available on Microscience drives. One type has nine (9) switches and one type has ten (10) switches. These switches are located on the drive PCB next to the power plug.

9 POSITION SWITCH: Pins 1, 2, and 4 should be in the "up" position .

Figure 10.2
Microscience
10 Position Switch



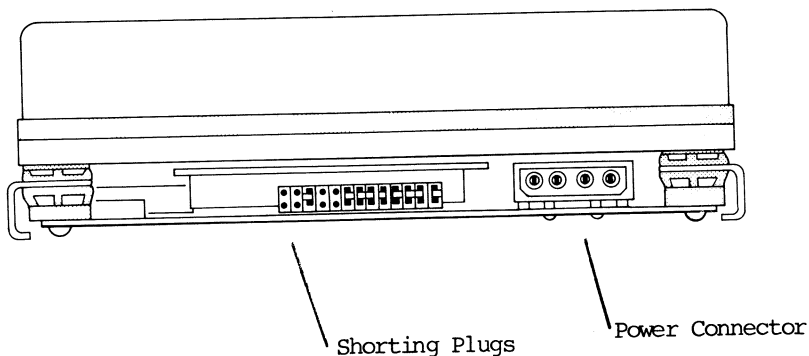
10 POSITION SWITCH: Pins 1, 3, 4, and 10 should be in the up position.

Refer to Figures 10.1 and 10.2 for Microscience drive configuration.

SHUGART

The Shugart hard drives should be jumpered according to figure 10.3. Next to the power connector on the rear of the drive, there are 13 sets of shorting plugs. With the rear of the drive facing you and the power connector on the right side, locate these shorting plugs. Starting on the right side (the side nearest the power connector), jumper the first eight sets of plugs, do not jumper the ninth and tenth sets, jumper the eleventh set, do not jumper the twelfth and thirteenth sets of plugs.

Figure 10.3
Shugart Hard Drive

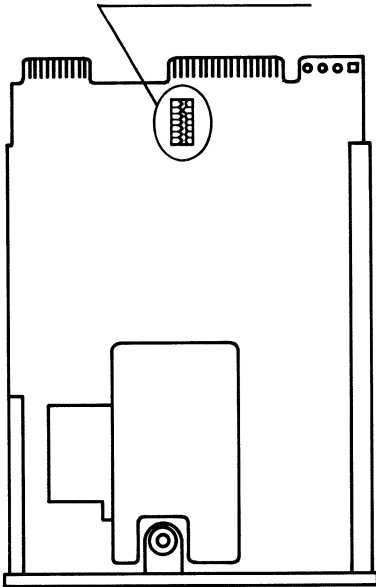


HARD DRIVE CONFIGURATION con't.

SEAGATE

Figure 10.4
Seagate Hard Drive

Option shunt block

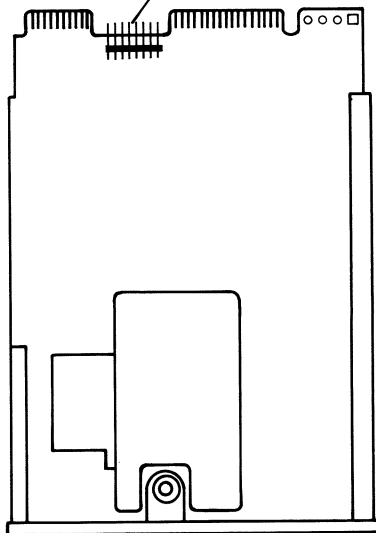


There are two types of option jumpers available on the Seagate drives. One is an option shunt block and the other is a row of jumper pins that are jumpered in pairs. Illustrations of both types are shown below for reference.

Next to the connector for the data cable, is a 16 pin option shunt block. Pins 7 and 10 should be jumpered.

Figure 10.5
Seagate Hard Drive

JUMPER OPTION



Next to the connector for the data cable is a double row of sixteen pins. With the front of the drive facing toward you and the P.C. board side facing up, jumper the two pins in the second position from the left. These are pins 7 and 10.

10.4 HARD DRIVE OR FLOPPY DRIVE REMOVAL FOR KAYPRO 10 OR 12X

For all hard drives and all diskette drives used in KAYPRO 10 and 12X computers.

Note: If the KAYPRO 10 has an Epson or Tokyo Electric diskette drive, it will be necessary to tilt the drive unit at an angle while removing or installing the drive. Refer to section 9.2 to determine the brand of drive.

Note: As you remove the cables on the KAYPRO 10 or 12X, it's a good idea to label them. This will insure that they get replaced correctly. Also, the end of the data cable that has a different color wire (usually red) goes to pin #1 on the connector.

1. Turn off the machine and disconnect the AC power (5.1).
2. Remove the chassis hood (5.1).
3. Remove the two diagonal braces from the right side of the chassis.
4. Tilt the machine up so that the front of the machine is sitting on the work surface and parallel to the floor.
5. Remove the six screws from the bottom of the chassis that are under the drives. Set the machine back down.
6. Remove the ribbon cables from the mainboard.
7. Slide entire drive shield unit slightly to the rear, to make room for the face of the floppy to clear the opening.
8. Lift entire drive shield unit slightly and slide out the side of the chassis.

REMOVING THE DRIVES FROM THE DRIVE SHIELD

1. Remove the ribbon cables, power plug and the ground wire from the back of the drive (hard drive or floppy). Label these.
2. Remove the two screws that attach the bottom of the drive to the drive shield (hard drive or floppy).
3. Remove the two screws that attach the top of the drive to the drive shield.
4. Slide drive out through the front of the drive shield.

HARD DRIVE OR FLOPPY INSTALLATION FOR KAYPRO 10 OR 12X

1. Configure the new drive. Refer to page 10—3 for the hard drive, pages 9—5 to 9—7 for the floppy drive.
2. Slide the drive into the shield so that the mounting holes in the drive are aligned with those in the shield.
3. Insert two screws through the top of the shield and into the mounting holes of the drive.
4. Tighten these securely.
5. Insert two screws through the bottom of the shield and into the mounting holes of the drive.
6. Tighten these securely.
7. Replace the ribbon cable(s), power plug and ground wire.
8. Slide the entire drive shield unit into the chassis.
9. Align the mounting holes in the drive shield with those in the chassis.
10. Insert the six screws through the mounting holes and tighten securely.
11. Replace the two diagonal braces.
12. Replace the ribbon cable(s) on the mainboard.
13. Replace the chassis hood.

11.0 HARD DISK CONTROLLER BOARD

11.1 DESCRIPTION

The hard disk controller board used in the KAYPRO 10 is a Western Digital board, and is not dealer-serviceable.

11.2 REMOVAL/INSTALLATION INSTRUCTIONS

HARD DRIVE CONTROLLER BOARD REMOVAL

1. Turn off the machine and disconnect AC power (5.1).
2. Remove chassis hood (5.1).
3. Remove the two diagonal braces from the right side of the machine.
4. Remove the power plug and the three ribbon cables from the hard disk controller board. Label these to insure correct replacement.
5. Remove the three screws that attach the hard disk controller board and the drive shield.
6. Remove the board.

HARD DRIVE CONTROLLER BOARD INSTALLATION

1. Position the hard disk controller board so that the mounting holes in the board are aligned with the holes in the drive shield.
2. Insert the three screws and tighten securely.
3. Replace the power plug and the three ribbon cables.
4. Replace the two diagonal braces.
5. Replace the chassis hood (5.1).

12.0 INTERFACE BOARD, KAYPRO 10 AND 12X

12.1 DESCRIPTION

The interface board that is used on the KAYPRO 10 and 12X is the interface between the mainboard and the hard drive controller board. This board is not dealer serviceable.

12.2 INTERFACE BOARD REMOVAL

1. Turn off the machine and disconnect the AC power (5.1).
2. Remove the chassis hood (5.1).
3. Remove the mainboard (6.7).
4. Remove the two ribbon cables from the interface board.
5. Remove the four screws that attach the interface board and the drive shield.
6. Remove the interface board.

INTERFACE BOARD INSTALLATION

1. Position the interface board so that the mounting holes on the board are aligned with the holes on the drive shield.
2. Insert four screws and tighten securely.
3. Replace the two ribbon cables.
4. Replace the mainboard (6.7).
5. Replace the chassis hood (5.1).

13.0 KEYBOARDS

The 76-key alphanumeric, detachable keyboard is connected to the computer by a four-wire cable and is powered by +5 VDC through the cable. The CAPS LOCK indicator light allows a quick check on whether the keyboard is receiving power.

The impedance of the connecting cable can be a critical factor in proper operation of the computer. Replacement of the standard cable with a phone cord can create malfunctions in signal transmission to the computer. This is because the wire in phone cords is too small; therefore, the impedance over the total length of the cord can be too high for reliable operation.

Wire in the standard keyboard cable is 28-gage copper, and the cable length is six feet. Should you desire a longer keyboard cable, restrict the length to not more than twelve feet, and use wire no smaller than 28 gauge.

Keyboard Cable Pinouts

Pin 4 (Black) - Serial Data out (to keyboard), TTL level.
Pin 3 (Red) - Ground
Pin 2 (Green) - Serial data in (from keyboard), TTL level.
Pin 1 (Yellow) - +5 VDC

14.0 KAYPRO ROBIE REMOVAL/INSTALLATION INSTRUCTIONS

14.1 CHASSIS COVER REMOVAL

1. Turn off the machine and disconnect the AC power(5.1).
2. Remove the four screws securing the top chassis cover (two on each side of the machine).
3. Remove the eight screws securing the bottom chassis cover (four on each side of the machine).
4. Remove the two sections of the chassis cover from the machine.

CHASSIS COVER REPLACEMENT

1. Place the lower chassis cover on the machine and align the eight mounting holes with the mounting holes in the sides of the machine.
2. Insert the eight screws into the mounting holes and tighten securely.
3. Place the top chassis cover on the machine and align the four mounting holes with the mounting holes in the sides of the machine.
4. Insert the four screws into the mounting holes and tighten securely.

14.2 DISKETTE DRIVE REMOVAL

1. Remove the top chassis cover (5.1).
2. Remove the drive support unit from the chassis. There are three screws in the front and three screws in the rear.
3. Remove the ribbon cable and the power plugs from the drives.
4. Lift the drive support unit (drives still attached) off the chassis.
5. To remove either drive from the drive support unit, turn the unit over and remove the four screws that attach the specific drive to the support unit.

DISKETTE DRIVE REPLACEMENT

1. Align the drive mounting holes with the mounting holes in the drive support unit.
2. Insert four screws into the drive support unit and into the drive. Tighten these screws securely.
3. Set the drive support unit on the chassis and align the mounting holes in the support unit with those in the chassis.
4. Insert three screws into the front mounting holes and three screws into the rear mounting holes. Tighten these securely.
5. Replace the ribbon cable and the power plugs on the drives.

—> TAB FOR THIS SECTION: TROUBLESHOOTING

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